Abstract—We present an LTS-based (Labelled Transition System) CSP-like (Communicating Sequential Processes) formalism for expressing parametrised systems. The parameters are process types, which determine the number of replicated components, and data types, which enable components with a parametrised state space. We prove that the formalism is compositional and show how to combine two existing results for parametrised verification in order to check trace refinement between parametrised processes. The combined approach gives upper bounds, i.e., cut-offs, for types such that a parametrised verification task collapses into finitely many checks solvable by using existing finite state refinement checking tools. We have implemented the approach and applied it to prove mutual exclusion properties of network protocols and systems with shared resources. To the best our knowledge, our technique is the only one that combines compositionality and completeness with support for multiple parametric process and data types.

I. INTRODUCTION

Concurrent and object-oriented software systems have many natural parameters which can take infinitely many values. Such parameters are the number of replicated parts, like processes and objects, and the size of data types, like message domains and address spaces. Consequently, the question on the correctness of a software system is naturally expressed as the parametrised verification problem: given a parametrised system implementation and specification, determine whether the implementation is correct with respect to the specification for all parameter values.

In practice, we can automatically verify only the smallest instances of a parametrised system in order to detect bugs in the implementation. Sometimes, we can also establish upper bounds, i.e., cut-offs, for the parameters such that the system implementation is correct for all parameter values if and only if it is correct for all values up to the cut-offs. In other words, if there is a bug in an implementation instance with a parameter value greater than the cut-off, then there is an analogous bug in an implementation instance where the values of the parameters are within the cut-offs. Although such cut-offs always exist, determining them algorithmically is undecidable in the most general case [1].

Another practical challenge is that some subsystems (e.g., external software packages and subsystems concurrently under construction) can be available only in interface specification form. Hence, there is a need to perform parametrised verification in a compositional way, where we first substitute the subsystem specification for each subsystem implementa-
can compute rough structural cut-offs for process types as before without paying attention to data types. After that, we can basically substitute the parallel composition of finitely many parametrised processes for a replicated parametrised parallel composition and apply the DI results to bound the size of data types. For each type \( T \), the cut-off size can be computed easily based on the structure of the parametrised system implementation and specification and the number of variables of the type \( T \). After that, the cut-offs can be further improved by analysing the instances up to the cut-offs at the operational level. Finally, the verification can be completed by solving the remaining finitely many finite state refinement checking tasks by using existing tools.

As a running example, we consider a host configuration protocol (HCP) with an arbitrary number of hosts (process type) and an arbitrary number of network addresses (data type). Since the protocol involves types of both kinds, it cannot be handled by either technique alone. However, it can be treated with our combined result, which implies that the result presented here is more powerful than either of the approaches alone. We have implemented the technique and, in addition to HCP, applied it to establish mutual exclusion properties for systems with shared resources. Since our formalism is compositional, it means that the specifications of these systems can be used in place of the implementations in further verification efforts.

Except for some small differences in formalism and the definition of cut-offs, our DI results are semantically similar to those of Lazic and Novak [8], [9]. Additionally, our DI proof technique is simpler than in [8], [9]: we use only the bijective mapping of transitions and avoid the construction of intermediate structures, like exploration graphs [8] and quotient LTSs [9], altogether. This leads to a more accessible and concise theory of data independence, which is the third contribution of the paper. However, unlike [8], [9], we consider only safety properties, since the PR technique cannot be extended to deadlock preserving semantics directly [5]. On the other hand, extending the DI part of our results to such semantics should be easy, since all the key DI lemmas are formulated at the operational level.

**Related Work:** The distinctive features of our approach are compositionality, completeness and support for the multiple parameters of two fundamentally different types.

Parametrised verification in a compositional setting is previously considered by Valmari & Tienari [10] and Creese [11]. In both works, induction is exploited to determine an abstract process which can be used in verification in place of the parametrised subsystem. The methods scale to the multi-parametrised case, too, but they involve the discovery of an invariant process which is a task that cannot be automated in general.

Ghilardi and Ranise present a complete parametrised verification technique [12], where the infinite family of finite state systems arising from a parametrised system description is encoded as a single infinite state well-structured transition system (WSTS) [13]. The technique allows for two levels of parametrisation, the number and the state space of processes, but since the formalism is not compositional, it does not allow for an arbitrary number of nested parametrised substructures like ours. The completeness of the approach is based on the fact that there is an order on the set of states compatible with the transition relation [13]. In our case, there is no such a criterion nor an (obvious) way to define a compatible order among the states of all the instances. That is because our processes communicate through alphabet-based synchronisation, where increasing the number of replicated components may block some transitions and hence break compatibility.

Other methods that enable multi-parametrisation verification are by Emerson & Kahlon [14], Yang & Li [15] and Hanna et al. [16]. Their approaches apply to systems with guarded broadcasts [14], shared actions [16] or rendezvous communication [15]. The methods are cut-off-based and allow for an arbitrary number of parameters which specify the number of replicated processes. However, none of them supports compositional analysis nor parametrised data types.

Other complete parametrised verification methods, in turn, are based on either cut-offs [17], [18], [19], [20], [21] or WSTSs [22], which covers counter abstraction and Petri nets [23]. WSTSs are already discussed earlier and the other cut-off results are only for closed systems with a single parameter determining the number of replicated processes.

Moreover, some of the results can be obtained in our approach, too. At least the systems with conjunctive guards [14] and homogeneous processes communicating through prioritised queue policy [21] can be modelled in our formalism [5]. On the one hand, most cut-off results including [14], [21] allow for the analysis of liveness, too, whereas our result as well as WSTS-based approaches are restricted to safety properties. On the other hand, parametrised systems where replicated processes communicate through rendezvous events are difficult to model in our formalism, whereas they can often be modelled as WSTSs.

**Outline:** In Section II, we introduce our process model. After that, we parametrise it while preserving compositionality. In Section IV, we present the cut-off results and finally, the paper concludes with discussion on future work. For the sake of readability and the lack of space, proofs are found in the online appendix [24].

II. PROCESSES

In this section, we define a fairly standard notation used throughout the paper and a CSP- and LTS-based process model with parallel composition and hiding operators. The main difference with the standard LTSs is that in our model, states and events have an explicit data part which makes adding parametrisation convenient.
Notation: For any set $A$, $A^*$ denotes the set of all finite tuples over $A$, especially the empty tuple ( ) is in $A^*$. Whenever $a := (a_1, \ldots, a_n)$ and $b := (b_1, \ldots, b_k)$ are tuples over $A$, then $|a|$ denotes the length $n$ of $a$, and $ab$ the concatenation $(a_1, \ldots, a_n, b_1, \ldots, b_k)$ of $a$ and $b$. For any function $f : A \rightarrow B$, $\text{dom}(f)$ is the domain $A$ of $f$, and $\text{im}(f)$ the image $\{f(a) \mid a \in A\}$ of $f$. The tuple $a$ can be regarded as a function $f : \{1, \ldots, n\} \rightarrow A$ such that $f(i) = a_i$ for all $i \in \{1, \ldots, n\}$, so we can define $\text{im}(a)$ as the set $\{a_i \mid i = 1, \ldots, n\}$. For any set $C$, $f|_C$ denotes the restriction of $f$ to $A \cap C$, i.e., a function $f' : A \cap C \rightarrow B$ such that $f'(a) = f(a)$ for all $a \in A \cap C$. For any function $g$ such that $\text{im}(f) \subseteq \text{dom}(g)$, $g \circ f$ is the composition of $g$ and $f$, i.e., a function $h : \text{dom}(f) \rightarrow \text{im}(g)$ such that $h(a) = g(f(a))$ for all $a \in A$.

Processes: A process is basically an LTS [3] where states have a control and data part and events have a channel and data part. Let $\mathbb{S}$, $\mathbb{C}$ and $\mathbb{V}$ be mutually disjoint countably infinite sets of control states, channels and constants, respectively, such that $\mathbb{S}$ contains the natural numbers and is closed under the Cartesian product. We assume that there is a single invisible channel $\tau \in \mathbb{C}$ which represents the internal activity of a process. The other channels are called visible and they are used for communication among processes and environment. A structure $s \mathbf{d}$, where $s \in \mathbb{S}$ and $\mathbf{d} \in \mathbb{V}^*$, is a state and a structure $c \mathbf{e}$, where $c \in \mathbb{C}$ and $\mathbf{e} \in \mathbb{V}^*$, is an event. If $c \neq \tau$, the event is visible, otherwise it is invisible.

Definition 1 (Process). A process $P$ is a pair $(s \mathbf{d}, R)$, where $s \mathbf{d}$ is a state and $R$ is a set of triples $(s \mathbf{d}, c \mathbf{e}, s' \mathbf{d}')$ such that $s \mathbf{d}, s' \mathbf{d}'$ are states and $c \mathbf{e}$ is an event.

The first component of $P$ is called the initial state, denoted by init($P$), and the second one is the set of transitions, denoted by tran($P$). The set of all the visible events occurring in $P$, the alphabet of $P$, is denoted by alph($P$). $P$ is (in)finite if and only if tran($P$) is. $P$ is (strongly) deterministic if (i) it does not involve invisible events and (ii) whenever $(s \mathbf{d}, c_1 \mathbf{e}_1, s_1 \mathbf{d}_1)$ and $(s \mathbf{d}, c_2 \mathbf{e}_2, s_2 \mathbf{d}_2)$ are transitions of $P$ such that $s_1 \mathbf{d}_1 \neq s_2 \mathbf{d}_2$, then $c_1 \mathbf{e}_1 \neq c_2 \mathbf{e}_2$.

Operators: In our formalism, both a system implementation and specification are modelled as processes $P_{impl}$ and $P_{spec}$, respectively. Since the implementation often consists of several components, $P_{impl}$ is typically a parallel composition of smaller processes, and before $P_{impl}$ is compared against $P_{spec}$, the channels irrelevant to $P_{spec}$ are hidden.

Definition 2 (Parallel Composition). Let $P_i$ be the process $(s_i \mathbf{d}_i, R_i)$ for both $i \in \{1, 2\}$. The parallel composition (of $P_1$ and $P_2$), denoted by $(P_1 \parallel P_2)$, is a pair $((s_1, s_2), (\mathbf{d}_1, \mathbf{d}_2), R_i)$, where $R_i$ is the set of all triples $((s_1, s_2), (\mathbf{d}_1, \mathbf{d}_2), c, (s_1', s_2', \mathbf{d}_1', \mathbf{d}_2'))$ such that (i) $c \neq \tau$ and $(s_1 \mathbf{d}_1, c \mathbf{e}, s_1' \mathbf{d}_1') \in R_1$ for all $i \in \{1, 2\}$, or (ii) $(s_1 \mathbf{d}_1, s_1' \mathbf{d}_1') \in R_1$, $c \notin \text{alph}(P_1)$, $s_2 \mathbf{d}_2$ is a state of $P_2$ if and only if $P_2 \parallel P_2$ is a process, where $P_1$ and $P_2$ execute a visible event jointly if and only if both agree on its execution, whereas the visible events only in the alphabet of one process and the invisible events are executed individually. This is essentially the parallel composition operator of CSP [2], [3] where the synchronisation alphabet is $\text{alph}(P_1) \cap \text{alph}(P_2)$. Note that the third component in the control states of $(P_1 \parallel P_2)$ tells which part of the data vector comes from $P_1$. Without the third component, the composition of states $s_1(a)$ and $s_2(a)$ and states $s_1()$ and $s_2(a)$ would be the same, which is not what we want.

Definition 3 (Hiding). Let $P$ be the process $(s \mathbf{d}, R)$ and $E$ a set of visible channels. The process $P$ after hiding $E$, denoted by $P \setminus E$, is a pair $(s \mathbf{d}, R_i)$, where $R_i$ is the set of (i) all triples $(s \mathbf{d}, c \mathbf{e}, s' \mathbf{d}') \in R$ such that $c \notin E$ and (ii) all triples $(s \mathbf{d}, \tau e, s' \mathbf{d}')$ such that $(s \mathbf{d}, c \mathbf{e}, s' \mathbf{d}') \in R$ for some $c \in E$.

In other words, $(P \setminus E)$ is a process obtained from $P$ by substituting $\tau$ for the channels $c$ in $E$.

Semantics: For verification purposes, a process is interpreted as a set of traces, which enables us to consider safety properties. A finite alternating sequence $(s_0 \mathbf{d}_0, e_1, s_1 \mathbf{d}_1, \ldots, e_n, s_n \mathbf{d}_n)$ of states and events of $P$ is a path of $P$ (from $s_0 \mathbf{d}_0$) to $s_n \mathbf{d}_n$ if $(s_{i-1} \mathbf{d}_{i-1}, c_1 \mathbf{e}_1, s_i \mathbf{d}_i)$ is a transition of $P$ for every $i \in \{1, \ldots, n\}$. A path from the initial state is called an execution (of $P$). A finite sequence of visible events is a trace of $P$, if there is an execution of $P$ such that the sequence is obtained from the execution by erasing all the states and the invisible events. The set of all the traces of $P$ is denoted by $\text{tr}(P)$. A process $P_1$ is a trace refinement of a process $P_2$, denoted by $P_1 \preceq_{tr} P_2$, if and only if $\text{alph}(P_1) = \text{alph}(P_2)$ and $\text{tr}(P_1) \subseteq \text{tr}(P_2)$ [2]. The processes $P_1$ and $P_2$ are trace equivalent, denoted by $P_1 \equiv_{tr} P_2$, if and only if $P_1 \preceq_{tr} P_2$ and $P_2 \preceq_{tr} P_1$. $P_{impl}$ is considered correct with respect to $P_{spec}$ if and only if $P_{impl} \preceq_{tr} P_{spec}$.

Calculus: The operators and the trace relations have many useful properties from the viewpoint of synthesis and analysis. First, the parallel composition is commutative and associative with respect to $\equiv_{tr}$ and a single state process $P_{id} := (s(), \emptyset)$ without transitions is an identity element of the parallel composition. That is why for every finite set $I = \{i_1, \ldots, i_n\}$ and any processes $P_{i_1}, \ldots, P_{i_n}$, we can define the $I$-indexed version of $\parallel$ as follows: $\langle i \in I \parallel P_i \rangle := P_{i_1} \parallel \ldots \parallel P_{i_n}$, when $n > 0$, and $\langle \emptyset \parallel P \rangle := P_{id}$, when $n = 0$. Secondly, $\preceq_{tr}$ is a preorder (i.e., a reflexive and transitive relation) and $\equiv_{tr}$ an equivalence on the set of processes. Moreover, $\preceq_{tr}$ is compositional, i.e., preserved under the application of the operators: if $P_1 \preceq_{tr} P_2$, then $P_1 \parallel P \preceq_{tr} P_2 \parallel P, P \parallel P \preceq_{tr} P \parallel P$ and $P \setminus E \preceq_{tr} P \setminus E$ for all processes $P_1, P_2, P$ and sets $E$ of visible channels. Hence, $\preceq_{tr}$ is a compositional preorder, a precongruence, on the set of processes.
III. PARAMETRISED PROCESSES

In this section, we equip our process calculus with parameters. Our parametrised formalism is based on the one which is presented in [4] and which allows for parametrisation of the structure of a system. Here, we extend that formalism with data parameters which allow for components with a parametrised state space, too.

Running Example: As a running example, we consider a host configuration protocol (HCP), where each host repeatedly picks a network address until it finds one that is not used by other hosts. This is done by broadcasting address queries and replies to other hosts in the style of ARP (Address Resolution Protocol). Our goal is to formally model the protocol with an arbitrary number of hosts and an arbitrarily large address space and prove that in our construction, each address is possessed by at most one host.

Parameters: In order to model HCP, we parametrise processes and operators with four kinds of variables: (process and data) types and (process and data) variables. A data type denotes a finite non-empty set of data values and a process type represents a finite non-empty set of the identifiers of replicated components of a certain kind whereas a process variable refers to the identifier of an individual component and a data variable to a data value. A (concurrent) parametrised system is composed of sequential parametrised processes each of which represents the system from the viewpoint of finitely many replicated components.

Formally, we assume that for each type $T$ there is a countably infinite set $I_T \subseteq \forall$ of constants such that $I_U$ and $I_V$ are disjoint whenever $U$ and $V$ are different types, and for each process and data variable $x$, there is respectively a unique process or data type $T_x$. The possible values of a type $T$ are the finite non-empty subsets of $I_T$ and the possible values of a variable $x$ are the elements of $I_{T_x}$. We assume that the sets of process types, data types, process variables and data variables, denoted by $\mathbb{T}_P$, $\mathbb{T}_D$, $\mathbb{X}_P$ and $\mathbb{X}_D$, respectively, are disjoint and countably infinite. We write $\top$ and $\bot$ short for $\mathbb{T}_P \cup \mathbb{T}_D$ and $\mathbb{X}_P \cup \mathbb{X}_D$, respectively.

In HCP, there are two kinds of replicated objects: hosts and addresses. Hence, we pick a process type $T_H$ to represent the set of the identifiers of hosts and a data type $T_A$ to denote the set of available addresses.

Guards: In our parametrised formalism, (in)equality tests between variables are represented as guards.

Definition 4 (Guard). Guards $C$ are given by the grammar $C ::= \top \mid x \equiv y \mid (\neg C) \mid (C \land C)$, where $x$ and $y$ range over variables.

The parameters of a guard $C$ are the variables occurring in $C$. The set of all the parameters of $C$ is denoted by $\text{par}(C)$ and the set $\text{par}(C) \cup \{T_x \mid x \in \text{par}(C)\}$ of all the parameters of $C$ plus the types of the parameters is denoted by $\overline{\text{par}}(C)$.

Intuitively, $\top$ denotes a guard which is always true and symbols with a hat on top are interpreted as connectives without one; the hat is included just to mark operators which act on parametrised structures. Formally, a guard is instantiated by using a function called a valuation which assigns values to parameters.

Definition 5 (Valuation). A valuation is a function $\phi$ whose domain is a finite set of types and variables such that (i) for each type $T \in \text{dom}(\phi)$, $\phi(T)$ is a finite non-empty subset of $I_T$ and (ii) for every variable $x \in \text{dom}(\phi)$, $T_x \in \text{dom}(\phi)$ and $\phi(x) \in \phi(T_x)$.

A valuation $\phi$ is compatible with a guard $C$ if and only if $\text{par}(C) \subseteq \text{dom}(\phi)$, i.e., $\overline{\text{par}}(\phi) \subseteq \text{dom}(\phi)$.

Definition 6 (Instance of Guard). Let $C$ be a guard and $\phi$ a compatible valuation. The $(\phi$-instance of $C$, denoted by $[C]_{\phi}$, is determined inductively as follows:

1) $[\top]_{\phi}$ equals $\phi(T)$, $[\bot]_{\phi}$ equals $\phi(T_x)$.
2) $[x \equiv y]_{\phi}$ equals $\phi(x) = \phi(y)$.
3) $[\neg C]_{\phi}$ is determined inductively as follows:
4) $[C_1 \land C_2]_{\phi}$ is $[C_1]_{\phi} \land [C_2]_{\phi}$.

Parametrised Processes: A structure $s_x$, where $s \in S$ and $x$ is a tuple of data variables, is a parametrised state. Respectively, a structure $c_y$, where $c \in \mathbb{C}$ and $y$ is a tuple of any variables, is a parametrised event. A sequential parametrised process is basically a process where parametrised states and events are substituted for the ordinary ones and transitions are equipped with a guard and a choice set, i.e., a set of data variables whose values are fixed during the execution of the transition.

Definition 7 (SPP). A sequential parametrised process (SPP) is a pair $S := (s_x, \Delta)$, where $s_x$ is a parametrised state and $\Delta$ is a finite set of five-tuples $(s_x, X, C, c_y, s'_x)$ such that $s_x$ and $s'_x$ are parametrised states, $c_y$ is a parametrised event, $C$ is a guard and $X \subseteq \text{im}(y') \cap X_D$ is a choice set.

The first component of $S$ is called the parametrised initial state and the elements of $\Delta$ are parametrised transitions. Let $x$ be a variable that occurs in a parametrised transition $(s_x, X, C, c_y, s'_x)$. If $x \in \text{im}(x') \cup X$, then $x$ and all its occurrences are bound in the parametrised transition, otherwise they are free. A variable is free in an SPP $S$, if it occurs in the parametrised initial state of $S$ or it is free in some parametrised transition of $S$. Respectively, a variable is bound in $S$, if it is bound in some parametrised transition of $S$. Hence, if a variable occurs twice in $S$, it can be both free and bound. The parameters of $S$ are the free variables and the types of bound variables occurring in $S$. Like in the case of guards, $\text{par}(S)$ denotes the set of all the parameters of $S$, $\overline{\text{par}}(S)$ is the set $\text{par}(S) \cup \{T_x \mid x \in \text{par}(S) \cap \mathbb{X}_D\}$ of all the parameters of $S$ plus the types of the parameters, and a valuation $\phi$ is compatible with $S$ if and only if $\text{par}(S) \subseteq \text{dom}(\phi)$, i.e., $\overline{\text{par}}(S) \subseteq \text{dom}(\phi)$. 
Analogously to predicate logic, the free occurrences of variables are initialised (instantiated) in the beginning and preserve their value throughout the computation whereas the values of the bound occurrences are determined at run time. To put it more formally, let \( \phi \) be a valuation and \( X \) a set of variables such that \( T_x \in \text{dom}(\phi) \) for every \( x \in X \). We write \( \text{ext}(\phi, X) \) for the set of all valuations \( \phi' \) with the domain \( \text{dom}(\phi) \cup X \) such that \( \phi \) and \( \phi' \) agree on the values of parameters outside \( X \), i.e., \( \phi'|_{\text{dom}(\phi) \setminus X} = \phi|_{\text{dom}(\phi) \setminus X} \) and \( \phi'(x) \in \phi(T_x) \) for all \( x \in X \).

**Definition 8 (Instance of SPP).** Let \( S \) be an SPP \((\hat{s}, \hat{\Delta}, \Delta)\) and \( \phi \) a compatible valuation. The \((\phi-)\text{instance of } S\), denoted by \([S]_\phi\), is a pair \((\hat{s}(\phi \circ \hat{x}), R)\), where \( R \) is the set of all triples \((s(\phi' \circ x), c(\phi' \circ y), s'(\phi' \circ x'))\) such that \((s, x, \Delta, c, y, s'x') \in \Delta\), \(\phi' \in \text{ext}(\phi, \text{im}(x) \cup X)\) and \([\circ]\_\phi\) is true.

In order to formalise the specification of HCP, we first capture it from the viewpoint of two hosts in an SPP \( UA2 \) in Figure 1, where a parametrised event \( ih(z_h, \alpha) \) denotes that a host \( z_h \) has an address \( \alpha \), guarders other than \( \top \) are written in brackets, and non-empty choice sets \( \{x_1, \ldots, x_n\} \) are expressed in the form \( \Box x_1, \ldots, x_n \) separated by a colon. In \( UA2 \), variables \( x_h \) and \( y_h \) of the type \( T_H \) representing addresses are free and variables \( x_a \) and \( y_a \) of the type \( T_A \) representing addresses are bound. That is why \( \text{par}(UA2) = \{x_h, y_h, T_A\} \) and \( \text{par}(UA2) = \{x_h, y_h, T_A, T_H\} \). Initially, \( UA2 \) allows the host \( y_h \) to report having any address but after the host \( x_h \) has picked an address \( x_a \), the host \( y_h \) is no longer allowed to report having \( x_a \).

![Figure 1. SPP UA2 representing the specification of HCP from the viewpoint of two hosts](image)

Concurrent parametrised processes are constructed from SPPs, guards and parametrised versions of the operators.

**Definition 9 (CPP).** Concurrent parametrised processes (CPPs) \( P \) are determined by the grammar:

\[
P ::= S \mid (C[P]) \mid (P || P) \mid (P \setminus E) \mid (\parallel \_ P),
\]

where \( S \) ranges over SPPs, \( C \) over guards, \( E \) over the finite sets of visible channels and \( x \) over process variables.

A process variable \( x \) is bound in a CPP \( P \) if it occurs in \( P \) and its every occurrence is within a structure of the form \( \parallel \_ P \) such that \( y = x \). A data variable \( x \) is bound in \( P \) if it occurs in \( P \) and it is not free in any SPP within \( P \). The other variables occurring in \( P \) are free in \( P \). The parameters of \( P \), the sets \( \text{par}(P) \) and \( \text{par}(P) \), and the notion of compatibility are defined like for SPPs. We can also write \( P(x_1, \ldots, x_m, T_1, \ldots, T_n) \) to point out that \( P \) is a CPP the parameters of which are variables \( x_1, \ldots, x_m \) and types \( T_1, \ldots, T_n \).

Intuitively, each CPP represents (infinitely) many processes obtained by fixing the values of the parameters and evaluating the operators. Especially, \( \parallel \_ \! P \) denotes the parallel composition of all structures \( P' \) obtained by letting the variable \( x \) range over its domain.

**Definition 10 (Instance of CPP).** Let \( P \) be a CPP and \( \phi \) a compatible valuation. The \((\phi-)\text{instance of } P\), denoted by \( [P]_\phi \), is determined inductively as follows:

1. \([P_1 || P_2]_\phi = [P_1]_\phi \parallel [P_2]_\phi\),
2. \([P \setminus E]_\phi = [P']_\phi \setminus E\), where \( E \) is a valuation,
3. \([C[P]]_\phi = \begin{cases} [P]_\phi, & \text{if } [C]_\phi \text{ is true}, \\ [P_{id}], & \text{if } [C]_\phi \text{ is false, and} \end{cases}\)
4. \([\parallel \_ P]_\phi = [\phi \in \text{ext}(\phi, x)](P')_\phi\).

The \((\phi-)\text{instance of } P \phi\) is denoted by \( P(\phi(x_1), \ldots, \phi(x_m), \phi(T_1), \ldots, \phi(T_n)) \), too. Nevertheless, \([P]_\phi \) is obviously a process with finitely many transitions and \( [P]_\phi = [P]_\psi \) whenever \( \psi \) is a valuation such that \( \psi|_{\text{par}(P)} = \phi|_{\text{par}(P)} \).

The model of the full specification is obtained by letting \( x_h \) and \( y_h \) to range over all pairs of different host identifiers and by composing all the resulting instances of \( UA2 \) in parallel. Hence, the formal specification is a CPP

\[
\text{UnqAdr}(T_H, T_A) ::= \Box x_h \parallel \Box y_h \Box (x_h \equiv y_h) UA2,
\]

which allows for each host to report only a single unique address. To see this, consider a valuation \( \phi \) such that \( \phi(T_H) = \{h_1, h_2\} \) and \( \phi(T_A) = A_2 \), where \( A_2 = \{a_1, a_2\} \). Obviously, \( \phi \) is compatible with \( \text{UnqAdr} \) and the \((\phi-)\text{instance of } UnqAdr \) is the process

\[
P_{id} \parallel (UA2(h_1, h_2, A_2) \parallel (UA2(h_2, h_1, A_2) \parallel P_{id})),
\]

which is equal to \( UA2(h_1, h_2, A_2) \parallel UA2(h_2, h_1, A_2) \) in Figure 2, modulo the structure of control states.

The protocol itself is modelled in a similar way and captured in a CPP

\[
\text{HCP}(T_H, T_A) ::= (\parallel \_ x_h \Box y_h \Box (x_h \equiv y_h) \text{Host}) \top IC,
\]

where \( \text{Host} \) is an SPP (in Figure 3 in [24]) and \( IC \) is the set of all visible channels occurring in \( \text{Host} \), except for \( \text{ih} \).

**Refinement:** We complete our parametrised formalism by defining a trace refinement relation on the set of CPPs.

**Definition 11 (Parametrised Trace Refinement).** A CPP \( P_1 \) is a trace refinement of a CPP \( P_2 \), denoted by \( P_1 \stackrel{\text{tr}}{\longrightarrow} P_2 \), if and only if \( [P_1]_\phi \preceq_{\text{tr}} [P_2]_\phi \) for all valuations \( \phi \) compatible with both \( P_1 \) and \( P_2 \).

Given a system implementation CPP \( P \) and a system specification CPP \( Q \), we consider \( P \) to be correct (with respect
The relation \( HCP \) set of CPPs. to specification does not involve hiding and that it is logically similar to the DI results \([8],[9]\). Theorem 20 is semantically similar to the DI results \([8],[9]\). respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. Theorem 16 is similar to the ones proved respectively. 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of SPPs occurring \( Q \). Hence, if the set of the processes of a (big) instance of \( Q \) equals the set of the processes of smaller instances of \( Q \), then by the commutativity, associativity and idempotence of the parallel composition, it is evident that the big instance is trace equivalent to the parallel composition of the small instances. For implementation CPPs \( P \), which typically involve hiding, trace equivalence does not hold in general. However, since distributing hiding over the parallel composition results in a process with more traces, we can still establish a trace refinement between the big instance of \( P \) and the parallel composition of the small ones.

**Lemma 13.** Let \( R \) be a CPP, \( \psi \) a compatible valuation, \( T \in \text{dom}(\psi) \) a process type and \( \Phi \) a finite set of the \( \{ T \} \)-subvaluations of \( \psi \) such that \( \text{prc}(R, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(R, \phi) \). Then \( [R]_\psi \preceq_{tr} \bigcup_{\phi \in \Phi} [R]_{\phi} \), and if \( R \) does not involve hiding, \( [R]_\psi =_{tr} \bigcup_{\phi \in \Phi} [R]_{\phi} \).

Considering our running example, let \( \theta \) and \( \Theta \) be as above. Since every element of \( \text{prc}(UnqAdr, \theta) \) depends on the identifiers of two hosts, it is easy to see that \( \text{prc}(UnqAdr, \theta) = \bigcup_{\theta' \in \Theta} \text{prc}(UnqAdr, \theta') \), which implies that \( [UnqAdr]_\theta =_{tr} \bigcup_{\theta' \in \Theta} [UnqAdr]_{\theta'} \). Similarly, we can check that \( \text{prc}(HCP, \theta) = \bigcup_{\theta' \in \Theta} \text{prc}(HCP, \theta') \), which implies that \( [HCP]_{\theta} =_{tr} \bigcup_{\theta' \in \Theta} [HCP]_{\theta'} \). Now, if \( [HCP]_{\theta} \preceq_{tr} [UnqAdr]_{\theta'} \) for all \( \theta' \in \Theta \), then by the compositionality of \( \preceq_{tr} \), we know that \( [HCP]_{\theta} \preceq_{tr} \bigcup_{\theta' \in \Theta} [UnqAdr]_{\theta'} \). Hence, the lemma implies that we can derive the correctness of a big system instance from the correctness of small instances.

**Proposition 14.** Let \( P \) and \( Q \) be CPPs such that \( Q \) does not involve hiding, \( \psi \) a compatible valuation, \( T \in \text{dom}(\psi) \) a process type and \( \Phi \) a set of the \( \{ T \} \)-subvaluations of \( \psi \) such that \( \text{prc}(P, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(P, \phi) \) and \( \text{prc}(Q, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(Q, \phi) \). If \( [P]_{\psi} \preceq_{tr} [Q]_{\psi} \) for all valuations \( \phi \in \Phi \), then \( [P]_{\psi} \preceq_{tr} [Q]_{\psi} \).

The proposition allows us to discard (big) instances but it does not explicitly say which instances we should keep. This piece of information is hidden in the condition of the form \( \text{prc}(R, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(R, \phi) \) which requires the set of the processes of the big instance to be the same as the set of the processes of the small ones. Since each element in \( \text{prc}(R, \psi) \) is completely determined by the values of finitely many variables and data types, the cut-off (size) for a process type \( T \) and \( R \), denoted by \( cp_T(R) \), depends on the maximum number of constants in \( I_T \) that occur in an element in \( \text{prc}(R, \psi) \). In other words, \( cp_T(R) \) is obtained by simply counting the number of the free variables of the type \( T \) and adding the maximum number of the nested bound variables of the type \( T \). More formally,

\[
cp_T(R) := \max(1, |\text{free}_T(R)| + \text{pbnd}_T(R)),
\]

where \( \text{free}_T(R) \) is the set \( \{ x \in X \cap \text{par}(R) \mid T_x = T \} \) of all the variables of the type \( T \) that are free in \( R \) and \( \text{pbnd}_T(R) \) is defined inductively as follows:

1. \( \text{pbnd}_T((\delta \xi, \Delta)) = 0 \),
2. \( \text{pbnd}_T(C[R'] = \text{pbnd}_T(R') \),
3. \( \text{pbnd}_T(R_1 \parallel R_2) = \max(\text{pbnd}_T(R_1), \text{pbnd}_T(R_2)) \),
4. \( \text{pbnd}_T(R' \setminus E) = \text{pbnd}_T(R') \) and
5. \( \text{pbnd}_T(\bigcup_{x = T} R') = \{ \text{pbnd}_T(R') + 1, \) if \( T_x = T \), \( \text{pbnd}_T(R') \), if \( T_x \neq T \).

For example, as the specification CPP \( UnqAdr \) involves two subprocesses of the form \( \bigcup_{x = T} P' \) such that \( T_x = T_H \), we see that \( \text{pbnd}_T(\bigcup_{x = T} R') = 2 \). Moreover, since \( UnqAdr \) has no free variable, it implies that \( \text{cp}_T(\bigcup_{x = T} R') = 2 \). Similarly, we can show that \( \text{cp}_T(\bigcup_{x = T} R') = 2 \).

**Lemma 15.** If \( R \) is a CPP, \( \psi \) a compatible valuation, \( T \in \text{dom}(\psi) \) a process type, \( k \geq |\text{im}(\psi)_{|T} \cap I_T| \) and \( \text{pbnd}_T(R) \) a positive integer and \( \Phi \) the set of all \( \{ T \} \)-subvaluations of \( \psi \) such that \( |\phi(T)| = |\text{im}(k, |\psi(T)|) | \), then \( \text{prc}(R, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(R, \phi) \).

By combining the results above, we get cut-offs for process types for parametrised trace refinement checking.

**Theorem 16** (Cut-Offs for Process Types). Let \( P \) and \( Q \) be CPPs such that \( Q \) does not involve hiding, \( \Phi \) the set of all valuations with the domain \( \text{prc}(P \parallel Q) \) and \( \Psi \) the set of all valuations \( \phi \in \Phi \) such that \( |\phi(T)| \leq \text{cp}_T(P \parallel Q) \) for every process type \( T \in \text{prc}(P \parallel Q) \). Then \( [P]_{\phi} \preceq_{tr} [Q]_{\phi} \) for all \( \phi \in \Phi \), if and only if \( [P]_{\psi} \preceq_{tr} [Q]_{\psi} \) for all \( \psi \in \Psi \).

When we apply the theorem to HCP, we see that to prove HCP correct for any number of hosts and network addresses, it is sufficient to consider instances with an arbitrary number of addresses but at most two hosts.

### B. Cut-Offs for Data Types

To determine cut-offs for data types, we first show that an execution of a big instance of a system implementation \( P \) can be bijectively mapped to an execution of a small instance of \( P \) by transition. (Here, big and small are in terms of the size of the values of data types.) If the small instance of \( P \) is a trace refinement of the small instance of the system specification \( Q \), then the small specification instance has a corresponding execution which can be projected back to the execution of the big instance of \( Q \) by using the inverse mappings, provided the specification is deterministic (Lemma 17). This implies that if the small instance of \( P \) is a trace refinement of the small instance \( Q \), then the big instance of \( P \) is a trace refinement of the big instance \( Q \), too (Prop. 18).

Since we can compute an upper limit for the size of small instances based on the cut-offs of process types and the number of data variables occurring in \( P \) and \( Q \) (Lemma 19), a parametrised trace refinement checking task reduces to the refinement checking of the instances of bounded size (Theorem 20).
The bijective mapping of executions between big and small instances presumes that the small instance is big enough. The sufficient size for the small instance depends on the number of constants occurring in a transition or the initial state of the big instance, which motivates the following definition. Let $P$ be a process and $A$ a set of constants. We write $\text{idc}(P,A)$ for the maximum number of constants in $A$ that occur in a state or transition of $P$, i.e.,

$$\text{idc}(P,A) := \max\{|\text{im}(d) \cap A|, |\text{im}(d\ell') \cap A| |$$

$$\hat{s}d = \text{init}(P), (s,d,e,e',s'd') \in \text{tran}(P)\}.$$ 

For example, let $\theta_1$ be a valuation with the domain $\{T_A, T_H\}$ such that $\theta_1(T_H) = \{h_1, h_2\}$ and $\theta_1(T_A) = \{a_1, \ldots, a_n\}$. If $n = 2$, then $[\text{UnqAdr}]_{\theta_1}$ is the process in Figure 2, where obviously $\text{idc}([\text{UnqAdr}]_{\theta_1}, T_A) = 2$. Moreover, since $\text{idc}([\text{UnqAdr}]_{\theta_1}, T_A) = 2$. More generally,

$$\text{idc}([\text{UnqAdr}]_{\theta_1}, T_A) = \max\{|\text{im}(d) \cap A|, |\text{im}(d\ell') \cap A| |$$

$$\hat{s}d = \text{init}(P), (s,d,e,e',s'd') \in \text{tran}(P)\}.$$ 

Lemma 17. Let $\mathcal{P}$ and $\mathcal{Q}$ be CPPs, $\psi$ a compatible valuation, $T \in \text{dom}(\psi)$ a data type and $\phi$ a $\{T\}$-subvaluation of $\psi$ such that $|\phi(T)| \geq |\text{im}(\psi|x)| \cap T | + \text{idc}([\mathcal{P}]_{\psi}, T | \text{im}(\psi|x)) + \text{idc}([\mathcal{Q}]_{\psi}, T | \text{im}(\psi|x)).$ Moreover, let $[\mathcal{Q}]_{\phi}$ be deterministic, $[\mathcal{P}]_{\phi}$ a trace refinement of $[\mathcal{Q}]_{\phi}$ and $(s_0, d_0, c_1 e_1, d_1, \ldots, c_n e_n, s_n, d_n)$ an execution of $[\mathcal{P}]_{\phi}$ such that $n = 0$ or $c_n e_n$ is a visible event, and let $i_1, i_2, \ldots, i_k$ be the increasing sequence of all the indices in $\{1, \ldots, n\}$ such that $c_i \neq \tau$.

Then, (i) there are bijections $g_1, \ldots, g_n : \mathbb{V} \rightarrow \mathbb{V}$ which move only constants in $\psi(T)$ \text{im}(\psi|x) such that $(s_0, d_0, c_1 e_1, d_1, \ldots, c_n e_n, s_n, d_n)$ is an execution of $[\mathcal{P}]_{\phi}$, (ii) there is an execution $(s_0, d_0, c_1 e_1, d_1, \ldots, c_n e_n, s_n, d_n)$ of $[\mathcal{Q}]_{\phi}$ such that $(s_0, d_0, c_1 e_1, d_1, \ldots, c_n e_n, s_n, d_n)$ is an execution of $[\mathcal{Q}]_{\phi}$ and $c_j (g_i^{-1} \circ e_j)$ for all $j \in \{1, \ldots, k\}$.

Regarding HCP, let $\theta_2$ be a $\{T_A\}$-subvaluation of $\theta_1$ such that $|\theta_2(T_A)| = 8$. Since $[\text{UnqAdr}]_{\theta_2}$ is deterministic, $|\text{im}(\psi|x)| \geq |\text{im}(\psi|x)) \cap T | + \text{idc}([\mathcal{HCP}]_{\theta_1}, T_A)$, the proposition implies that if $|\text{HCP}|_{\theta_2} \preceq_{TR} [\text{UnqAdr}]_{\theta_1},$ then for every execution of $[\mathcal{HCP}]_{\theta_1}$ there is an execution of $[\text{UnqAdr}]_{\theta_1}$ which gives rise to the same trace. Hence, with the aid of the lemma above, we can derive the correctness of a big implementation instance from the correctness of a small one.

Proposition 18. Let $\mathcal{P}$ and $\mathcal{Q}$ be CPPs, $\psi$ a compatible valuation, $T \in \text{dom}(\psi)$ a data type and $\phi$ a $\{T\}$-subvaluation of $\psi$ such that $|\phi(T)| \geq |\text{im}(\psi|x)| \cap T | + \text{idc}([\mathcal{P}]_{\psi}, T | \text{im}(\psi|x)) + \text{idc}([\mathcal{Q}]_{\psi}, T | \text{im}(\psi|x)).$ If $[\mathcal{Q}]_{\phi}$ is deterministic and $[\mathcal{P}]_{\phi} \preceq_{TR} [\mathcal{Q}]_{\phi},$ then $[\mathcal{P}]_{\psi} \preceq_{TR} [\mathcal{Q}]_{\psi},$ too.

Like in the case of process types, the proposition allows us to discard (big) instances but it does not explicitly say which instances we should keep. This time, the cut-off size of a data type $T$ depends on the number of constants in $I_T$ that occur in a state or transition of an implementation instance and a specification instance. If the size of process types is already bounded using Theorem 16, then the cut-off (size) for $T$ and a CPP $\mathcal{R},$ denoted by $\text{idc}_{TR}(\mathcal{R})$, can be over-approximated as follows. First, count the number of the free variables of the type $T$. Then, go through all SPPs $S$ occurring in $\mathcal{R}$ and in each round, add the product of two figures: the maximum number of the bound variables of the type $T$ occurring in a parametrised transition and the maximum number of the instances of $S$ within an instance of $\mathcal{R}$. To put it more formally,

$$\text{idc}_{TR}(\mathcal{R}) := \max\{|\text{free}_{TR}(\mathcal{R})| + \text{dbnd}_{TR}(\mathcal{R})\},$$

where $\text{dbnd}_{TR}(\mathcal{R})$ is defined inductively as follows:

1. $\text{dbnd}_{TR}(\mathcal{R}) = \text{idc}([\mathcal{P}]_{\psi}, T | \text{im}(\psi|x));$
2. $\text{dbnd}_{TR}(\mathcal{R}) \preceq_{TR} \text{dbnd}_{TR}(\mathcal{R})$;
3. $\text{dbnd}_{TR}(\mathcal{R}) \preceq_{TR} \text{dbnd}_{TR}(\mathcal{R})$;
4. $\text{dbnd}_{TR}(\mathcal{R}) \preceq_{TR} \text{dbnd}_{TR}(\mathcal{R})$;
5. $\text{dbnd}_{TR}(\mathcal{R}) \preceq_{TR} \text{dbnd}_{TR}(\mathcal{R})$.

For example, let $\mathcal{R} := \mathcal{HCP} \sqcup \text{UnqAdr}$. By above, we know that $\text{cp}_{\theta_2}(\text{HU}) = 2$. Moreover, since every parametrised transition of $\text{UA2}$ involves at most two variables of the type $T_A$, we see that $\text{dbnd}_{TA,HU}(\text{UA2}) = 2$. Hence, $\text{dbnd}_{TA,HU}(\text{UA2}) = \text{cp}_{\theta_2}(\text{HU}) \cdot \text{cp}_{\theta_2}(\text{HU})$. Similarly, we can show that $\text{dbnd}_{TA,HU}(\mathcal{HCP}) = 8$. Because $\text{HU}$ has no free variable, it implies that $\text{dbnd}_{TA,HU}(\mathcal{HCP}) = 16$.

Lemma 19. Let $\mathcal{P}$ be a CPP, $\phi$ a compatible valuation and $\mathcal{R}$ a CPP such that $|\phi(U)| \leq \text{cp}_{\theta_2}(\mathcal{R})$ for each process type $U \in \text{dom}(\phi)$. Then for every data type $T \in \text{dom}(\phi)$, $\text{dbnd}_{TR}(\mathcal{P}) \geq \text{idc}([\mathcal{P}]_{\psi}, T | \text{im}(\psi|x));$

By combining the results above, we get cut-offs for data types for parametrised trace refinement checking.

Theorem 20 (Cut-Offs for Data Types). Let $\mathcal{P}$ and $\mathcal{Q}$ be CPPs such that $\mathcal{Q}$ is deterministic. Moreover, let $\mathcal{R}$ be the set of all valuations $\psi$ with the domain $\text{var}(\mathcal{P} \sqcup \mathcal{Q})$ such that $|\phi(U)| \leq \text{cp}_{\theta_2}(\mathcal{P}) \sqcup \mathcal{Q}$ for every process type $U \in \text{var}(\mathcal{P} \sqcup \mathcal{Q})$, and $\Phi$ the set of all valuations $\phi \in \mathcal{Q}$ such that $|\phi(T)| \leq \text{idc}_{TR}(\mathcal{P} \sqcup \mathcal{Q})$ for every data type $T \in \text{var}(\mathcal{P} \sqcup \mathcal{Q})$. Then, $[\mathcal{P}]_{\psi} \preceq_{TR} [\mathcal{Q}]_{\psi}$ for all $\psi \in \Phi$, if and only if $[\mathcal{P}]_{\phi} \preceq_{TR} [\mathcal{Q}]_{\phi}$ for all $\phi \in \Phi$.

When we apply the theorem to HCP, we see that to prove the system correct for any number of addresses and up to two hosts, it is sufficient to consider instances with at most 16 addresses and two hosts.
C. Automated Approach

Even though Theorems 16 and 20 provide cut-offs for types, they do not quite enable automated parametrised verification because there are still infinitely many instances up to the cut-offs. However, if we consider only valuations which are non-isomorphic, i.e., cannot be obtained from each other by the bijective mapping of constants, then we are left with finitely many instances. Formally, valuations $\phi_1$ and $\phi_2$ are isomorphic, if they have the same domain and if there is a bijection $g : \mathcal{V} \rightarrow \mathcal{V}$ such that $\phi_2(x) = g(\phi_1(x))$ for all variables $x \in \text{dom}(\phi_1)$ and $\phi_2(T) = \{g(c) \mid c \in \phi_1(T)\}$ for all types $T \in \text{dom}(\phi_1)$. Valuations are non-isomorphic if they are not isomorphic.

Getting rid of isomorphs is safe since isomorphic valuations result in verification tasks with the same answer. That why we say that a set $\Phi$ of valuations is a cut-off set for a CPP $\mathcal{R}$ if $\Phi$ is a maximal set of non-isomorphic valuations $\phi$ with the domain $\text{dom}(\mathcal{R})$ such that $|\phi(U)| \leq cp_{tr}(\mathcal{R})$ for every process type $U \in \text{dom}(\phi)$ and $|\phi(T)| \leq cd_{tr}(\mathcal{R})$ for every data type $T \in \text{dom}(\phi)$. With Theorems 16 and 20, this notion leads to our main result, which allow us to reduce a parametrised verification task to finitely many refinement checks between finite processes.

Corollary 21 (The Main Cut-Off Theorem). Let $\Phi$ be a cut-off set for a CPP $(\mathcal{P} \parallel \mathcal{Q})$, where $\mathcal{Q}$ is deterministic and does not involve hiding. Then (1) $\Phi$ is finite and (2) $\mathcal{P} \prec_{tr} \mathcal{Q}$ if and only if $\mathcal{P} \parallel \mathcal{Q} \prec_{tr} \mathcal{Q}$ for all $\phi \in \Phi$.

Another practical problem is that establishing the determinism of the specification involves infinitely many checks, too. That is why we provide cut-offs for that task as well by using similar reasoning as above.

Theorem 22 (Cut-Offs for Determinism Checking). Let $\Phi$ be a cut-off set for a CPP $(\mathcal{P} \parallel \mathcal{Q})$, where $\mathcal{Q}$ does not involve hiding. Then (1) $\Phi$ is finite and (2) $\mathcal{Q}$ is deterministic, if and only if $\mathcal{P} \parallel \mathcal{Q} \prec_{tr} \mathcal{Q}$ for all $\phi \in \Phi$.

Corollary 21 and Theorem 22 give rise to a completely automatic procedure for parametrised verification which we have implemented in the recent version of the Bounds tool [26]. First, the tool reads system implementation and specification CPPs. Then, it computes the cut-off sizes for types, produces the instances of the specification up to the cut-offs and checks that they are deterministic. Moreover, since the cut-offs provided by the corollary are only rough structural ones, the tool tries to improve them further by checking the assumptions of Propositions 14 and 18 for each instance up to the rough bounds and by discarding big instances which satisfy the assumptions of either proposition. After that, Bounds produces the trace refinement checking tasks up to the improved cut-offs. Finally, the outputted finite state verification questions are solved using the refinement checker FDR2 [3] in order to obtain the answer to the parametrised verification task.

When applied to HCP, Bounds first generates all 32 instances up to the cut-offs of two hosts and 16 addresses and checks that the specification is deterministic. After that, it applies Propositions 14 and 18, which reveals that only six of the instances have to be actually verified. By using FDR2, all six instances are found to be correct, which implies that HCP operates correctly for any number of hosts and addresses.

We have also proved mutual exclusion properties for a shared resource system (SRS) where an arbitrary number of users compete for an access to an arbitrary number of shared resources which store data from an arbitrary large domain and a cache coherence protocol with an arbitrary number of processors and an arbitrary amount of memory storing arbitrary large data values. Additionally, we have proved consistency properties for both systems when the number of users and processes was fixed to two. In each case, the whole verification process took only a second or two (see Table I), which shows that our approach is applicable to practical verification problems. Moreover, since our formalism is compositional, the specifications of these systems can be used in place of the implementations in further verification efforts. The experiments were run on a desktop computer with 4GB of memory running 64-bit Ubuntu on 3Ghz Intel Core2 Duo processor. The descriptions of HCP, SRS and Cache models are available in [24].

V. CONCLUSIONS AND FUTURE WORK

We have presented a formalism for expressing parametrised systems and combined two existing results [8], [5] on parametrised verification into a single powerful technique which enables parametrised verification by reduction to finitely many finite state verification tasks.

The reduction is determined by the structure of a system implementation and specification and, to the best of our knowledge, this is the first and only parametrised verification technique that allows for compositional reasoning, lends support to multiple and two fundamentally different kinds of parameters and guarantees termination with the correct answer. The approach is implemented in a tool used to prove the correctness of several parametrised systems.

An obvious topic for future research is extending the approach. We already know that the approach can be extended with relations over process types that are definable in the first order logic [7], [5] but we are also interested in mixing process and data types and extending the technique to liveness properties [10] and interface theories [27]. The challenge is that such extensions tend to render parametrised refinement checking undecidable [5]. Nevertheless, we are hopeful in finding practically sensible assumptions under which the extensions can be realised.

Acknowledgement: The work was partially funded by Helsinki Institute for Information Technology HIIT,
Table 1

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Academy of Finland (project 139402), and RECOMP project funded by ARTEMIS-JU.

REFERENCES


A. Additional Definitions, Lemmas and Propositions

For the proofs, we need the following auxiliary concepts and results. For any process $P$, we write $st(P)$ for the set of the states of $P$ and $g(P)$ for a process obtained from $P$ by mapping the constants using a function $g : \mathbb{V} \rightarrow \mathbb{V}$. For any valuation $\phi$, we write $\text{Im}(\phi)$ for the set $\bigcup_{T \in \text{dom}(\phi)}(\phi(T))$ of all constants occurring in the image of $\phi$. Notation $g \circ \psi$, where $\psi$ is a valuation and $g : \mathbb{V} \rightarrow \mathbb{V}$ a bijection, denotes a function $\phi$ with the domain $\text{dom}(\psi)$ such that $\phi(x) = g(\psi(x))$ for all variables $x \in \text{dom}(\psi)$ and $\phi(T) = \{g(v) \mid v \in \psi(T)\}$ for all types $T \in \text{dom}(\psi)$. Hence, valuations $\phi_1$ and $\phi_2$ are isomorphic if there is a bijection $g : \mathbb{V} \rightarrow \mathbb{V}$ such that $\phi_2 = g \circ \phi_1$.

**Proposition 23.** Let $P_1$, $P_2$ and $P_3$ be processes, $E$ a set of visible channels and $g$ a bijection: $\mathbb{V} \rightarrow \mathbb{V}$. Then the following holds.

1. $P_1 || P_2 \equiv_{tr} P_2 || P_1$,
2. $P_1 || (P_2 || P_3) \equiv_{tr} (P_1 || P_2) || P_3$,
3. $P_1 || P_1 \equiv_{tr} P_1$,
4. if $\alpha(\phi(P_2)) = \emptyset$, then $P_1 || P_2 \equiv_{tr} P_1$,
5. $P_1$ is deterministic if and only if $g(P_1)$ is deterministic,
6. $P_1 || P_2$ is deterministic if and only if both $P_1$ and $P_2$ are deterministic.
7. $g(P_1 || P_2) = g(P_1) || g(P_2)$,
8. $g(P_1 \setminus E) = g(P_1) \setminus E$,
9. $(P_1 || P_2) \setminus E \preceq_{tr} (P_1 \setminus E) || (P_2 \setminus E)$, and
10. if $P_1 \preceq_{tr} P_2$, then $P_1 || P_3 \preceq_{tr} P_2 || P_3$, $g(P_1) \preceq_{tr} g(P_2)$ and $P_1 \setminus E \preceq_{tr} P_2 \setminus E$.

**Proposition 24.** Let $P$ be a CPP and $\phi$ a compatible valuation. Then (i) $[P]_{\phi}$ is a finite process and (ii) $[P]_{\phi} = [P]_{\psi}$ whenever $\psi$ is a valuation such that $\psi_{\text{par}(P)} = \phi_{\text{par}(P)}$.

**Lemma 25.** Let $C$ be a guard, $\psi$ a compatible valuation and $\phi$ a subvaluation of $\psi$. Then $[C]_{\phi}$ is true if and only if $[C]_{\psi}$ is true.

**Lemma 26.** Let $S$ be an SPP, $\psi$ a compatible valuation and $\phi$ a $T \setminus \psi$-subvaluation of $\psi$. Then $[S]_{\psi} = [S]_{\phi}$.

**Lemma 27.** Let $C$ be a guard, $\phi$ a compatible valuation and $g$ a bijection: $\mathbb{V} \rightarrow \mathbb{V}$ such that $g(I_T) = I_T$ for every type $T$. Then $[C]_{\phi}$ if and only if $[C]_{g \circ \phi}$.

**Lemma 28.** Let $R$ be a CPP, $\phi$ a compatible valuation, $T$ a data type and $g$ a bijection on $\mathbb{V}$ which moves only constants in $\phi(T) \setminus \text{im}(\phi_{|X})$, then $[R]_{\phi} = g([R]_{\phi})$.

**Proposition 29.** If $R$ is a CPP, $\phi$ a compatible valuation, $T$ a data type and $g$ a bijection on $\mathbb{V}$ which moves only constants in $\phi(T) \setminus \text{im}(\phi_{|X})$, then $[R]_{\phi} = g([R]_{\phi})$.

**Lemma 30.** Let $R$ be a CPP, $\psi$ a compatible valuation, $T \in \text{dom}(\psi)$ a data type and $\phi$ a $\{T\}$-subvaluation of $\psi$ such that $|\phi(T)| \geq |\text{idc}([R]_{\psi} \setminus I_T \setminus \text{im}(\psi_{|X})) + |\text{im}(\psi_{|X})| \cap I_T|$. Then (i) the initial state of $[R]_{\psi}$ is the initial state of $[R]_{\phi}$; and (ii) whenever $d, d'$ and $e$ are tuples of constants over $\text{Im}(\phi)$, then $(s, d, c, e, s'd') \in \text{tran}([R]_{\phi})$ if and only if $(s, d, c, e, s'd') \in \text{tran}([R]_{\psi})$.

**Lemma 31.** Let $\Phi$ be a set of non-isomorphic valuations with the same finite domain and $k$ a positive integer such that $|\phi(T)| \leq k$ for all valuations $\phi \in \Phi$ and types $T \in \text{dom}(\phi)$. Then the set $\Phi$ is finite.

**Proposition 32.** Let $P$ and $Q$ be CPPs and $\phi$ and $\psi$ isomorphic valuations compatible with the CPPs. If $[P]_{\phi} \preceq_{tr} [Q]_{\psi}$ then $[P]_{\psi} \preceq_{tr} [Q]_{\psi}$, too.

**B. Proofs**

**Proposition 23**: Most of the properties are well known [2], [3]. See [5] for proofs.

**Proposition 24**: The proposition follows straightforwardly from the definition of the instance.

**Proof**: The relation $\preceq_{tr}$ is obviously reflexive because $[P]_{\phi} \preceq_{tr} [P]_{\phi}$ for all CPPs $P$ and valuations $\phi$ such that $\text{par}(P) \subseteq \text{dom}(\phi)$.

To see that $\preceq_{tr}$ is also transitive, let $P_1, P_2, P_3$ be CPPs such that $P_1 \preceq_{tr} P_2$ and $P_2 \preceq_{tr} P_3$. Moreover, let $\phi$ be a valuation compatible with $P_1$ and $P_3$ and $\phi'$ any valuation with the domain $\text{dom}(\phi) \cup \text{par}(P_2)$ such that $\phi'_{|\text{dom}(\phi)} = \phi$. Note that such a valuation always exists. By assumption, $[P_1]_{\phi'} \preceq_{tr} [P_2]_{\phi'}$ and $[P_2]_{\phi'} \preceq_{tr} [P_3]_{\phi'}$, which by the transitivity of $\preceq_{tr}$ implies that $[P_1]_{\phi'} \preceq_{tr} [P_3]_{\phi'}$.

By Proposition 24, it means that $[P_1]_{\phi} \preceq_{tr} [P_3]_{\phi}$, too. Therefore, $P_1 \preceq_{tr} P_3$, which implies that $\preceq_{tr}$ is transitive.

Finally, we need to show that $\preceq_{tr}$ is compositional. For that purpose, let $P_1$ and $P_2$ be CPPs such that $P_1 \preceq_{tr} P_2$.

If $C$ is a guard and $\phi$ a valuation compatible with $C, P_1$ and $P_2$, then there are two cases to consider. If $[C]_{\phi}$ is false, then obviously $[[C]_{P_1}]_{\phi} \preceq_{tr} [[C]_{P_2}]_{\phi}$. On the other hand, if $[C]_{\phi}$ is true, then $[[C]_{P_1}]_{\phi} = [P_1]_{\phi} \preceq_{tr} [P_2]_{\phi} = [[C]_{P_2}]_{\phi}$. Hence, $P_1 \preceq_{tr} P_2$ implies that $[C]_{P_1} \preceq_{tr} [C]_{P_2}$, too.

Similarly, we can show that $P_1 \preceq_{tr} P_2$ implies $P_1 \preceq_{tr} P_2, P_1 || P_2 \preceq_{tr} P_1 \setminus E \preceq_{tr} P_2 \setminus E$ and $P_1 \setminus E \preceq_{tr} P_2$ for all CPPs $P$, all process variables $x$ and all finite sets $E$ of visible channels. Therefore, the relation $\preceq_{tr}$ is also compositional and hence a precongruence.

**Lemma 25**: The lemma follows easily by induction on the structure of $C$ by using the lemma as an induction hypothesis.

**Lemma 26**: Since process variables can occur only free in $S$, it is obvious that both $[S]_{\psi}$ and $[S]_{\phi}$ have the same initial state and the same set of transitions.

**Lemma 13**: First, we show that when $R$ does not involve hiding, $[R]_{\psi} \equiv_{tr} [R]_{\phi}$. We argue by induction on the structure of $R$ by using the claim as an induction hypothesis.

In the base case, $R$ is an SPP. Since $\text{prc}(R, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(R, \phi)$, it means that $[R]_{\psi} = [R]_{\phi}$ for all $\phi \in \Phi$. 
Since every process is an idempotent with respect to the parallel composition (Prop. 23, Item 3), it implies that $[\mathcal{R}]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi$.

In the induction step, there are three cases to consider. First, let us assume that $\mathcal{R} = \mathcal{C}|\mathcal{R}'$. Since $\phi$ is a subvaluation of $\psi$ for all $\phi \in \Phi$, by Lemma 25, $[\mathcal{C}]_\psi = [\mathcal{C}]_\phi$ for every $\phi \in \Phi$. If $[\mathcal{C}]_\psi$ is not true, then $[\mathcal{R}]_\psi = [\mathcal{R}]_\phi = P_{id}$ for all $\phi \in \Phi$. By the idempotence of the parallel composition (Prop. 23, Item 3), it implies that $[\mathcal{R}]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi$. Otherwise, if $[\mathcal{C}]_\psi$ is true, $[\mathcal{R}]_\psi = [\mathcal{R}]_\phi$ and $[\mathcal{R}]_\phi = [\mathcal{R}]_\phi$ for all $\phi \in \Phi$. It also means that

$$prc(\mathcal{R}', \psi) = prc(\mathcal{R}, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}', \phi).$$

Hence, by the induction hypothesis,

$$[\mathcal{R}]_\psi = [\mathcal{R}]_\phi \equiv_{tr} \{ \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi \} = \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi.$$

Next, we assume that $\mathcal{R} = \mathcal{R}_1 \upharpoonright \mathcal{R}_2$. Since $prc(\mathcal{R}, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi)$, it implies that $prc(\mathcal{R}_1, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}_i, \phi)$ for both $i \in \{1, 2\}$. Then, by the induction hypothesis, $[\mathcal{R}_i]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi} [\mathcal{R}_i]_\phi$ for both $i \in \{1, 2\}$. By the commutativity and associativity of the parallel composition (Prop. 23, Items 1–2) and the compositional property of the trace equivalence (Prop. 23, Item 10),

$$[\mathcal{R}]_\psi = [\mathcal{R}_1]_\psi \parallel [\mathcal{R}_2]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi} [\mathcal{R}_1]_\phi \parallel [\mathcal{R}_2]_\phi \equiv_{tr} \bigcup_{\phi \in \Phi} \bigcup_{\phi' \in \Phi} [\mathcal{R}]_{\phi'} = \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi.$$

By the transitivity of the trace equivalence, it implies that $[\mathcal{R}]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi} [\mathcal{R}]_\phi$.

Finally, let $\mathcal{R}$ be $\mathcal{R}_1 \upharpoonright \mathcal{R}'$. For every $\psi' \in ext(\psi, \{x\})$, let $\Phi_{\psi'}$ denote the set of all $\phi' \in \bigcup_{\phi \in \Phi} ext(\phi, \{x\})$ such that $\phi'(x) = \psi'(x)$. Obviously, $\Phi_{\psi'}$ is a finite set of $\{T\}$-valuations of $\psi'$ for every $\psi' \in ext(\psi, \{x\})$. Moreover, since $prc(\mathcal{R}, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi)$, then $prc(\mathcal{R}, \psi')$ must be $\bigcup_{\phi' \in \Phi_{\psi'}} prc(\mathcal{R}, \phi')$ for all $\psi' \in ext(\psi, \{x\})$, too. By the induction hypothesis, it implies that $[\mathcal{R}]_{\psi'} \equiv_{tr} \bigcup_{\phi \in \Phi_{\psi'}} [\mathcal{R}]_\phi$. Then, by the commutativity, associativity and idempotence of the parallel composition (Prop. 23, Items 1–3), the identity of $P_{id}$ (Prop. 23, Item 4) and the compositional property of the trace equivalence (Prop. 23, Item 10),

$$[\mathcal{R}]_\psi = \bigcup_{\psi' \in ext(\psi, \{x\})} [\mathcal{R}]_{\psi'} \equiv_{tr} \bigcup_{\phi \in \Phi_{\psi'}} [\mathcal{R}]_\phi.$$

By the transitivity of the trace equivalence, it implies that $[\mathcal{R}]_\psi \equiv_{tr} \bigcup_{\phi \in \Phi_{\psi'}} [\mathcal{R}]_\phi$. Hence,

$$[\mathcal{P}]_\psi \leq_{tr} [\mathcal{P}]_\phi \equiv_{tr} [\mathcal{Q}]_\phi \equiv_{tr} [\mathcal{Q}]_\phi \equiv_{tr} [\mathcal{Q}]_\phi,$$

which by the transitivity of the trace refinement implies that $[\mathcal{P}]_\psi \leq_{tr} [\mathcal{Q}]_\psi$.

**Lemma 15:** We argue by induction on the structure of $\mathcal{R}$ by using the lemma as an induction hypothesis.

In the base step, $\mathcal{R}$ is an SPP $S$. First, note that $\Phi$ is non-empty because $\min(k, |\psi(T)|) \geq |\im(\psi|x) \cap I_T|$. Secondly, since each $\phi \in \Phi$ is a $\{T\}$-subvaluation of $\psi$, by Lemma 26, it means that $[S]_{\psi} = [S]_{\phi}$ for every $\phi \in \Phi$. Then, by definition, it is evident that $prc(\mathcal{R}, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi)$.

In the induction step, there are four cases to consider. First, let us assume that $\mathcal{R} = \mathcal{C}|\mathcal{R}'$. Since $\phi$ is a subvaluation of $\psi$ for all $\phi \in \Phi$, by Lemma 25, $[\mathcal{C}]_\psi = [\mathcal{C}]_\phi$ for every $\phi \in \Phi$. If $[\mathcal{C}]_\psi$ is false, then $prc(\mathcal{R}, \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi) = \emptyset$. Otherwise, if $[\mathcal{C}]_\psi$ is true, then by definition and the induction hypothesis,

$$prc(\mathcal{R}, \psi) = prc(\mathcal{R}', \psi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}', \phi) = \bigcup_{\phi \in \Phi} prc(\mathcal{R}, \phi).$$

Next, let $\mathcal{R}$ be $\mathcal{R}_1 \upharpoonright \mathcal{R}_2$. Obviously, $k \geq |\im(\psi|x) \cap I_T| + \text{pbnd}_T(\mathcal{R}) \geq |\im(\psi|x) \cap I_T| + \text{pbnd}_T(\mathcal{R}_i)$ for both $i \in \{1, 2\}$.
\( \{1,2\} \), which means that the induction hypothesis is applicable to \( R_1 \) and \( R_2 \). Hence, \( \text{prc}(R_i, \psi) = \bigcup_{\phi \in \Phi} \text{prc}(R_i, \phi) \) for both \( i \in \{1,2\} \), which implies that

\[
\text{prc}(\psi) = \bigcup_{i \in \{1,2\}} \left( \{i\} \times \text{prc}(R_i, \psi) \right) = \bigcup_{i \in \{1,2\}} \left( \{i\} \times \bigcup_{\phi \in \Phi} \text{prc}(R_i, \phi) \right) = \left( \bigcup_{\phi \in \Phi} \text{prc}(R, \phi) \right) \cup \bigcup_{i \in \{1,2\}} \left( \{i\} \times \text{prc}(R_i, \phi) \right).
\]

After that, we assume that \( R \in R' \setminus E \). Again, it is easy to see that \( k \geq \max(\psi(x) \cap \Pi) + \text{pbnd}_T(\psi) \geq \max(\psi(x) \cap \Pi) + \text{pbnd}_T(\psi') \). Secondly, for every \( \psi' \in \text{ext}(\psi, \{x\}) \), let \( \Phi_{\psi'} \) be the set of all \( \psi' \) such that \( \psi(x) = \psi'(x) \). Obviously, \( \Phi_{\psi'} \) is a set of \( \{T\} \)-subvaluations \( \phi' \) of \( \psi' \) such that \( \phi'(x) = \min(k, |\psi(x)|) \). To see that \( \Phi_{\psi'} \) is the set of all \( \{T\} \)-subvaluations \( \phi' \) of \( \psi' \) such that \( \phi'(x) = \min(k, |\psi(x)|) \). By the induction hypothesis, it means that \( \text{prc}(R', \psi') = \bigcup_{\phi \in \Phi_{\psi'}} \text{prc}(R', \phi') \) for all \( \psi' \in \text{ext}(\psi, \{x\}) \). Therefore,

\[
\text{prc}(\psi) = \bigcup_{\psi' \in \text{ext}(\psi, \{x\})} \left( \{\psi'(x)\} \times \text{prc}(R', \psi') \right) = \bigcup_{\psi' \in \text{ext}(\psi, \{x\})} \left( \{\psi'(x)\} \times \bigcup_{\phi \in \Phi_{\psi'}} \text{prc}(R', \phi') \right) = \bigcup_{\phi' \in \text{ext}(\psi, \{x\})} \left( \{\phi'(x)\} \times \text{prc}(R', \phi') \right) = \bigcup_{\phi' \in \Phi_{\psi'}} \text{prc}(R', \phi').
\]

Hence, by the induction principle, the lemma is correct. ■

\textbf{Theorem 16.} Obviously, if \([P]_\psi \preceq_{tr} [Q]_\psi \) for all \( \psi \in \Psi \), then \([P]_\psi \preceq_{tr} [Q]_\psi \) for all valuations \( \psi \in \Psi \), too.

Next, let us assume that \([P]_\psi \preceq_{tr} [Q]_\psi \) for all valuations \( \psi \in \Psi \). Let \( T_1, \ldots, T_n \) be the process types in \( \text{pr}(P \upharpoonright Q) \) and \( \Psi_0, \ldots, \Psi_n \) be the sets of valuations of the domain \( \text{pr}(P \upharpoonright Q) \) such that \( \Psi_0 \subseteq \Psi \), \( \Psi_n = \Phi \) and for all \( i \in \{1, \ldots, n\} \), \( \Psi_i \) is the set of all \( \{T_i\} \)-subvaluations \( \psi_{i-1} \) of the valuations in \( \Psi_i \) such that \( |\psi_{i-1}(T_i)| \leq \text{cpd}_T(P \upharpoonright Q) \). We proceed by induction to show that for all \( i \in \{0, \ldots, n\} \), the relation \([P]_{\psi_i} \preceq_{tr} [Q]_\psi \) holds whenever \( \psi_i \in \Psi_i \).

The base step is clear since \( \Psi_0 \subseteq \Psi \) and the claim holds by assumption. In the induction step, let \( \psi_i \in \Psi_i \setminus \Psi_{i-1} \), \( k := \text{cpd}_T(P \upharpoonright Q) \) and \( \Phi_i \) be the set of all \( \{T_i\} \)-subvaluations \( \phi \) of \( \psi_i \) such that \( |\phi(T_i)| = k \). By definition, \( k \geq \max(\text{free}_T(P \upharpoonright Q) + \text{pbnd}_T(P \upharpoonright Q)) \geq \max(\text{free}_T(P \upharpoonright Q) + \text{pbnd}_T(P \upharpoonright Q)) \) and by the choice of \( \psi_i \), \( |\phi(T_i)| = k \) for all \( \phi \in \Phi_i \). By Lemma 15, it implies that \( \text{prc}(Q, \psi_i) = \bigcup_{\phi \in \Phi_i} \text{prc}(Q, \phi) \) and \( \text{prc}(P, \psi_i) = \bigcup_{\phi \in \Phi_i} \text{prc}(P, \phi) \). Since \( \Phi_i \subseteq \Psi_{i-1} \), by the induction hypothesis, \([P]_{\psi_i} \preceq_{tr} [Q]_\psi \) for all valuations \( \psi \in \Phi_i \). By Proposition 14, it means that then \([P]_{\psi_i} \preceq_{tr} [Q]_\psi \) holds especially for all valuations \( \phi \in \Phi_i \).

\textbf{Lemma 27.} The claim follows straightforwardly by induction on the structure of the guard using the lemma as an induction hypothesis.

\textbf{Lemma 28.} We argue by induction on the structure of \( R \) by using the lemma as an induction hypothesis.

In the base step, \( R \) is an SPP and it is easy to see that \( g([R]_\phi) = [R]_{g_\phi} \).

In the induction step, there are four cases to consider. First, we assume that \( R \in [C]_\psi \). By Lemma 27, we know that \( [C]_{\psi_1} \) if and only if \( [C]_{g_\phi} = \text{false} \). Then \( [R]_{\psi_1} = [R]_\phi = P_{ad} \) and the lemma holds trivially. On the other hand, if \( [C]_{\psi_1} = \text{true} \), then by the induction hypothesis, \( g([R]_\phi) = [R]_{g_\phi} = [R]_{g_\phi} \).

Next, if \( R \in [R_1 \bowtie R_2] \), then by Item 7 of Proposition 23 and the induction hypothesis,

\[
g([R]_\phi) = g([R_1]_\phi \bowtie [R_2]_\phi) \overset{P_{13.7}}{=} g([R_1]_\phi \bowtie [R_2]_\phi) \overset{\text{Item 8 of Proposition 23}}{=} [R_1]_{g_\phi} \bowtie [R_2]_{g_\phi} = [R]_{g_\phi}.
\]

After that, let \( R \in R' \setminus E \). Then by the induction hypothesis, \( g([R']_\phi) = [R']_{g_\phi} \). By Item 8 of Proposition 23, it implies that

\[
g([R]_\phi) = g([R']_\phi \setminus E) \overset{P_{13.8}}{=} g([R']_\phi \setminus E) \overset{\text{Item 8 of Proposition 23}}{=} [R']_{g_\phi} \setminus E = [R]_{g_\phi}.
\]
Finally, if $R$ is $\langle R', \psi \rangle$, we argue like in the second case of the induction step. By using Item 7 of Proposition 23 and the induction hypothesis, we see that
\[
g([R]_\phi) = g([R']_\phi)
\]
\[\overset{P3.7}{\equiv} \phi' \in \text{ext}(\phi, \{x\})
\]
\[
g([R']_\phi) \text{ th } \phi' \in \text{ext}(\phi, \{x\})
\]
\[
\phi' \in \text{ext}(\phi, \{x\}) \implies [R']_\phi' = [R]_\phi'.
\]

Hence, also the induction step is clear and by the induction principle, the lemma is correct.

**Proposition 29:** Since $g \circ \phi = \phi$, by Lemma 28, $[R]_\phi = [R]_\phi' = g([R]_\phi)$.

**Lemma 30:** We argue by induction on the structure of $R$ by using the lemma as an induction hypothesis.

In the base case, $R$ is an SPP $(\mathcal{A}, \psi, \mathcal{X})$. Since $\psi$ is compatible with $R$ and $\phi$ is a subvaluation of $\psi$, $\phi_{\text{im}(\mathcal{A})} = \psi_{\text{im}(\mathcal{A})}$. Hence, $\phi \circ x = \psi \circ x$, which implies that the initial states of $[R]_\phi$ and $[R]_\psi$ are the same.

Next, let $d, d', e \in (\text{Im}(\phi))^*$. Since $\phi$ is a subvaluation of $\psi$, it is easy to see that whenever $(s, d, c, e, s', d')$ is a transition of $[R]_\phi$, $(s, d, e, s', d')$ is a transition of $[R]_\psi$ too. To prove the other direction, let $(s, d, c, e, s', d')$ be a transition of $[R]_\psi$. By definition, there are $x, y, \mathcal{X} \subseteq \mathcal{A}$, a subset $X$ of $\text{Im}(\mathcal{X})$ and $\phi' \in \text{ext}(\phi, \{x\}) \cup \mathcal{X}$ such that $d = \phi' \circ x, e = \phi' \circ y, d' = \phi' \circ x'$ and $[C]\phi' = \text{true}$. Since $d, d', e \in (\text{Im}(\phi))^*$ and $\phi$ is a subvaluation of $\psi$, we can pick $\phi' \in \text{ext}(\phi, \text{Im}(\mathcal{X}) \cup X)$ such that $\phi'$ and $\psi'$ agree on the values of the variables in $\text{Im}(\mathcal{X}) \cup \text{par}(\mathcal{C})$. It means that $d = \phi' \circ x, e = \phi' \circ y, d' = \phi' \circ x'$ and $[C]\phi' = \text{true}$, which implies that $(s, d, e, s', d')$ is a transition of $[R]_\phi$ as well.

In the induction step, there are four cases to consider. First, we assume that $R$ is $C[R']$. Because $\psi$ is compatible with $R$ and $\phi$ is a subvaluation of $\psi$, by Lemma 25, it implies that $[C]_\phi$ if and only if $[C]_\psi$ is $\text{true}$. Hence, there are two cases to check. If $[C]_\phi = [C]_\psi = \text{false}$, then $[R]_\phi = [R]_\psi = P_{id}$ and it is obvious that the lemma holds. On the other hand, if $[C]_\phi = [C]_\psi = \text{true}$, then $[R]_\phi = [R]_\psi = [R]_\psi$. Now, by the induction hypothesis, it is again obvious that the lemma holds.

Next, we assume that $R$ is $R_1 \parallel R_2$. In order to apply the induction hypothesis to both $R_1$ and $R_2$, we need to show that $[\phi(T)] \geq \text{idc}([R]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X})) \setminus I_T$ for both $i \in \{1, 2\}$. For that purpose, let $(s_1, d_1, c, e, s_2, d_2')$ be a transition of $[R_1]_\phi$. If $c, e \in \text{alph}([R_2]_\phi)$, then there are $s_2, d_2, s', d_2' \in \text{st}([R]_\phi)$ such that $(s_2, d_2, c, e, s', d_2')$ is a transition of $[R_2]_\phi$. If $c, e \notin \text{alph}([R_2]_\phi)$, then $(s_1, s_2, c, e, s', d_2')$ is a transition of $[R_1]_\phi$ and $[R_2]_\phi$, which implies that $([s_1, s_2, c, e, s', d_2']) \in \text{st}(\text{Im}(\psi|\mathcal{X})) \setminus I_T$. Since the initial state of $[R]_\phi$ is the combination of the initial states of $[R_1]_\phi$ and $[R_2]_\phi$, this implies that $\text{idc}([R_2]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X})) \setminus I_T \geq \text{idc}([R_2]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X})) + \text{Im}(\psi|\mathcal{X}) \cap I_T$. Similarly, one can establish $[\phi(T)] \geq \text{idc}([R_2]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X}) \cap I_T)$. Now, by the induction hypothesis, $\text{init}([R_2]_\phi) = \text{init}([R_2]_\phi)$ for both $i \in \{1, 2\}$, which implies that $[R_2]_\phi$ and $[R_2]_\psi$ have the same initial state.

Let us then assume that $(s_1, s_2, d_1, d_2) + (s_1, s_2, d_1, d_2) \in \text{trans}([R_1]_\phi)$, which implies that $(s_1, s_2, d_1, d_2) \in \text{trans}([R_2]_\phi)$ for both $i \in \{1, 2\}$, then by the induction hypothesis, $(s_1, s_2, d_1, d_2) \in \text{trans}([R_2]_\phi)$, which implies that $(s_1, s_2, d_1, d_2) \in \text{trans}([R_2]_\phi)$, too. Otherwise, there are distinct $i, j \in \{1, 2\}$ such that $(s_i, s_j, d_i, d_j) \in \text{trans}([R_2]_\phi)$, $a \in \text{alph}([R_2]_\phi)$.

To see that $a \notin \text{alph}([R_2]_\phi)$, assume contrarily that $a \in \text{alph}([R_2]_\phi)$, then by the induction hypothesis, $a \in \text{trans}([R_2]_\phi)$, too. Therefore, $a \in \text{trans}([R_2]_\phi)$.

Proving the other direction, i.e., the containment $\text{trans}([R_2]_\phi) \subseteq \text{trans}([R_2]_\phi)$, proceeds similarly. The main difference is that now it is easy to see that a state of $[R_2]_\phi$ is a state of $[R_2]_\psi$, too, while proving that $a \notin \text{alph}([R_2]_\phi)$ implies $a \notin \text{alph}([R_2]_\phi)$ is a similar process as establishing $[s_1, s_2, d_1, d_2] \in \text{trans}([R_2]_\phi)$ above.

Thirdly, we assume that $R$ is $R' \setminus E$. Since hiding affects only channel names, it is evident that $\text{idc}([R_2]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X})) = \text{idc}([R_2]_\phi, I_T \setminus \text{Im}(\psi|\mathcal{X}))$, which implies that the induction hypothesis is applicable to $R'$. By definition and induction hypothesis, it is then straightforward to check that $[R_2]_\phi$ and $[R_2]_\psi$ have the same initial state.
If \((s, d, c, e, s', d')\) is a transition of \([R]_\psi\) such that \(d, d' \in \text{Im}(\phi)\), then there are two cases to consider. If 
\((s, d, c, e, s', d') \in \text{trn}(\langle R \rangle_\psi)\) and \(c \notin E\), then the induction hypothesis implies that 
\((s, d, e, s', d') \in \text{trn}(\langle R' \rangle_\psi)\), as well.
Therefore, \((s, d, c, e, s', d')\) is a transition of \([R]_\psi\), too. Otherwise, \(c = \tau\) and there is \(e' \in E\) such that 
\((s, d, e', s', d') \in \text{trn}(\langle R' \rangle_\psi)\). By the induction hypothesis, it implies that 
\((s, d, c, e', s', d') \in \text{trn}(\langle R' \rangle_\psi)\), which, in turn, means that \((s, d, c, e, s', d')\) is a transition of \([R]_\psi\) as well in this case. Proving \(\text{trn}(\langle R \rangle_\psi) \subseteq \text{trn}(\langle R \rangle_\psi)\), proceeds similarly.

The case when \(R = \bigcup_x R'\) is a generalisation of the case when \(R = R_1 \parallel R_2\) and can be treated in a similar way. That is because \(x\) is a process variable, so for every \(\psi' \in \text{Ext}(\psi, \{x\})\) there is \(\phi' \in \text{Ext}(\phi, \{x\})\) such that \(\phi'\) is a subvaluation of \(\psi'\). Hence, also this case is clear and, by the induction principle, the lemma is correct.

Lemma 17: We argue by induction on \(K\) using the lemma as an induction hypothesis.

In the base case, \(K = 0\), which implies that the execution \(s_0 d_0, c_1 e_1, s_1 d_1, \ldots, c_m e_m, s_m d_m\), of \([Q]_\psi\), is an execution of \([R]_\psi\). Hence, the base case is clear.

In the induction step, \(K\) is a positive integer. Now, we can apply the induction hypothesis to the execution 
\((s_0 d_0, c_1 e_1, s_1 d_1, \ldots, c_m e_m, s_m d_m)\), where \(m = k - 1\).

Hence, (i) there are bijections \(g_1, \ldots, g_m : \mathcal{V} \rightarrow \mathcal{V}\) which move only constants in \(\psi(T) \setminus \text{Im}(\psi(x))\) such that 
\((s_0 d_0, c_1 g_1 \circ e_1, s_1 g_1 \circ d_1, \ldots, c_m g_m \circ e_m, s_m g_m \circ d_m)\) is an execution of \([Q]_\psi\) and (ii) there is an execution 
\((s_0 d_0, c_1 e_1', s_1 e_1', \ldots, c_m e_m, s_m e_m)\), of \([Q]_\psi\) such that 
\((s_0 d_0, c_1 g_1 \circ e_1, s_1 g_1 \circ d_1, \ldots, c_m g_m \circ e_m)\) is an execution of \([R]_\psi\). To prove the lemma, we need to show that \(\phi(T) = \phi(T)\).

First, if \(c e \in \text{alp}(\langle R \rangle_\psi)\), there are states \(s_1 d_1, s_1 d_1\) such that \((s_1 d_1, c e, s_1 d_1)\) is a transition of \([R]_\psi\). Since 
\(|\phi(T)| \geq \text{idc}(\langle R \rangle_\psi, \mathcal{V} \setminus \text{Im}(\psi(x)))\), and maps those in \(\text{Im}(\psi(x)) \setminus \text{Im}(\psi(x))\) to \(\text{Im}(\psi(x))\). Then, by Proposition 29 and Lemma 30, 
\((s_1 g_1 \circ e_1, c g \circ e_1, s_1 g_1 \circ d_1)\) is a transition of \([R]_\psi\).

Hence, \(c e \in \text{alp}(\langle R \rangle_\psi)\), too. Similarly, one can show that every event in \(\text{alp}(\langle R \rangle_\psi)\) is also an event in \(\text{alp}(\langle R \rangle_\psi)\), which implies that the alphabets of \([Q]_\psi\) and \([P]_\psi\) match.
such that \((c'_1 e'_1, \ldots, c'_k e'_k)\) equals \((c_1 e_1, \ldots, c_n e_n)\) after erasing the invisible events. Therefore, \(t \in \text{tr}(\{Q\}_\psi)\), too, which proves that the lemma is correct.

**Lemma 19:** We argue by induction on the structure of \(P\) by using the lemma as an induction hypothesis.

In the base step, \(P\) is an SPP \((s, \delta, \Delta)\). Since \(\text{im}(s) \subseteq \text{dom}(\phi)\), the set \(\text{im}(\phi \circ s) \cap (I_T \setminus \text{im}(\phi|x))\) is empty. Moreover, it is easy to see that for every \((s, x, C, e, y, s', x') \in \Delta\) and \(\phi' \in \text{ext}(\phi, \text{im}(x) \cup X)\) such that \([C]_{\phi'}\) is true it holds that

\[
\{|x \in \text{im}(x) \cup X \mid T_x = T| \geq |\phi'(x) \in I_T \mid x \in \text{im}(x) \cup X| \geq |\text{im}(\phi' \circ xyx) \cap (I_T \setminus \text{im}(\phi|x))| ,
\]

which means that \(\text{dbnd}_{T,R}(P) \geq \text{idc}([P']_{\phi}, I_T \setminus \text{im}(\phi|x))\) for every data type \(T \in \text{dom}(\phi)\). Hence, the base step is clear.

In the induction step, there are four cases to consider. First, we assume that \(P = [C]_{\phi'}\). If \([C]_{\phi} = \phi\) is false, then \([P']_{\phi} = P_{id}\). Since \(\text{idc}(P_{id}, I_T \setminus \text{im}(\phi|x)) = 0\) for every data type \(T \in \text{dom}(\phi)\), it is obvious that the claim holds. Otherwise, \([C]_{\phi}\) is true and \([P']_{\phi} = [P']_{\phi}\). Then, by the induction hypothesis,

\[
\text{dbnd}_{T,R}(P) = \text{dbnd}_{T,R}(P')
\]

\[
\geq \text{idc}([P']_{\phi}, I_T \setminus \text{im}(\phi|x)) = \text{idc}([P]_{\phi}, I_T \setminus \text{im}(\phi|x)) ,
\]

for all data types \(T \in \text{dom}(\phi)\), which implies that this case is clear.

Next, we assume that \(P = P_1 P_2\). First, note that every state of \([P]_{\phi}\) consists of a state of \([P_1]_{\phi}\) and a state of \([P_2]_{\phi}\). Secondly, note that every transition of \([P_1]_{\phi}\) consists of either a transition of \([P_1]_{\phi}\) and a transition of \([P_2]_{\phi}\) or a transition of \([P_1]_{\phi}\) and a state of \([P_j]_{\phi}\), where \(i \neq j\) and \(i, j\) are different integers in \(\{1, 2\}\). Therefore, it is obvious that

\[
\text{idc}([P]_{\phi}, I_T \setminus \text{im}(\phi|x)) \leq \sum_{i \in \{1, 2\}} \text{idc}([P_i]_{\phi}, I_T \setminus \text{im}(\phi|x))
\]

for every data type \(T \in \text{dom}(\phi)\). Therefore, by the induction hypothesis,

\[
\text{dbnd}_{T,R}(P) = \text{dbnd}_{T,R}(P_1) + \text{dbnd}_{T,R}(P_2) 
\]

\[
\geq \sum_{i \in \{1, 2\}} \text{idc}([P_i]_{\phi}, I_T \setminus \text{im}(\phi|x)) 
\]

\[
\geq \text{idc}([P]_{\phi}, I_T \setminus \text{im}(\phi|x))
\]

for all data types \(T \in \text{dom}(\phi)\), so also this case is clear.

After that, we assume that \(P = P' \setminus E\). Since hiding affects only channel names, obviously \(\text{idc}([P]_{\phi}, I_T \setminus \text{im}(\phi|x)) = \text{idc}([P']_{\phi}, I_T \setminus \text{im}(\phi|x))\). Then, by the induction hypothesis, it is easy to see that the claim holds in this case, too.

Finally, we consider the case when \(P\) is \(\hat{\pi}_x P'\). By the induction hypothesis, we know that \(\text{dbnd}_{T,R}(P') \geq \text{idc}([P']_{\phi}, I_T \setminus \text{im}(\phi|x))\) for every \(\phi' \in \text{ext}(\phi, \{x\})\) and all data types \(T \in \text{dom}(\phi')\). Since

\[
\text{cp}_{P_1}(R) \geq |\phi(T_x)| = |\text{ext}(\phi, \{x\})| ,
\]

it implies that

\[
\text{dbnd}_{T,R}(P) = \text{cp}_{P_1}(R) \cdot \text{dbnd}_{T,R}(P') 
\]

\[
\geq |\text{ext}(\phi, \{x\})| \cdot \text{dbnd}_{T,R}(P') 
\]

\[
\geq \sum_{\phi' \in \text{ext}(\phi, \{x\})} \text{idc}([P']_{\phi'}, I_T \setminus \text{im}(\phi|x))
\]

whenever \(T \in \text{dom}(\phi)\) is a data type. Now, we argue like in the second case of the induction step that every constant in a state of \([P']_{\phi}\) occurs in a state of some \([P']_{\phi'}\), and every constant in a transition of \([P]_{\phi}\) occurs in a state or a transition of some \([P']_{\phi'}\), where again \(\phi' \in \text{ext}(\phi, \{x\})\). Moreover, since \(x\) is a process variable, it is obvious that \(I_T \setminus \text{im}(\phi'|x) = I_T \setminus \text{im}(\phi|x)\) for all data types \(T \in \text{dom}(\phi)\). Therefore,

\[
\sum_{\phi' \in \text{ext}(\phi, \{x\})} \text{idc}([P']_{\phi'}, I_T \setminus \text{im}(\phi'|x)) 
\]

\[
\geq \text{idc}([P]_{\phi}, I_T \setminus \text{im}(\phi|x))
\]

for every data type \(T \in \text{dom}(\phi)\), which completes the induction step. Hence, by the induction principle, the lemma is correct.

**Theorem 20:** Since \(\Phi \subseteq \Psi\), it obvious that when \([P]_{\psi} \leq_{tr} [Q]_{\psi}\) for all valuations \(\psi \in \Psi\), then \([P]_{\phi} \leq_{tr} [Q]_{\phi}\) for all valuations \(\phi \in \Phi\), too.

Next, let us assume that \([P]_{\phi} \leq_{tr} [Q]_{\phi}\) for all valuations \(\phi \in \Phi\) and let \(\psi \in \Psi\). To prove the theorem, it is sufficient to show that then \([P]_{\psi} \leq_{tr} [Q]_{\psi}\) too. First, we pick data types \(T_1, \ldots, T_n \in \text{tr}(P \parallel Q)\) and valuations \(\psi_0, \ldots, \psi_n \in \Psi\) such that \(\psi_0 = \Phi, \psi_n = \psi\) and for all \(i \in \{1, \ldots, n\}\), \(\psi_{i-1}\) is a \(\{T_i\}\)-subvaluation of \(\psi_i\) and \(|\psi_{i-1}(T_i)| = c_{dbnd}(P \parallel Q)\). After that, we proceed by induction to show that \([P]_{\psi_i} \leq_{tr} [Q]_{\psi_i}\) for all \(i \in \{1, \ldots, n\}\).

The base step is clear since \(\psi_0 \in \Phi\) and the claim holds by the assumption. In the induction step, we apply the definition and Lemma 19 to see that

\[
|\psi_{i-1}(T_i)| = c_{dbnd}(P \parallel Q) 
\]

\[
= \max(1, \text{free}_{T}(P) \cup \text{free}_{T'}(Q)) 
\]

\[
+ \text{dbnd}_{T,R}(P) + \text{dbnd}_{T,R}(Q) 
\]

\[
\geq |\{x \in \text{dom}(\psi_{i|x}) \mid T_x = T_i\}| 
\]

\[
+ \text{idc}([P]_{\phi_i}, I_T \setminus \text{im}(\psi_{i|x})) 
\]

\[
+ \text{idc}([Q]_{\phi_i}, I_T \setminus \text{im}(\psi_{i|x})) 
\]
\[
\begin{align*}
\geq & \left| \text{im}(\psi_i|_X) \cap I_T \right| \\
& + \text{idc}([P]_{\psi_i}, I_T, \text{im}(\psi_i|_X)) \\
& + \text{idc}([Q]_{\psi_i}, I_T, \text{im}(\psi_i|_X)) .
\end{align*}
\]

Since \([Q]_{\psi_i} \) is deterministic and, by the induction hypothesis, \([P]_{\psi_i} \preceq_{\text{tr}} [Q]_{\psi_i} \), it follows from Proposition 18 that \([P]_{\psi_i} \preceq_{\text{tr}} [Q]_{\psi_i} \) for all \( i \in \{0, \ldots, n\} \), which means that \([Q]_{\psi_i} \) for all \( i \in \{0, \ldots, n\} \), and for all types \( T \in \text{dom}(P \parallel Q) \), there is an infinite subset \( \Phi' \) of \( \Phi \) such that all valuations in \( \Phi' \) map each type in the domain to a set of the same size, i.e., \( |\phi_1(T)| = |\phi_2(T)| \) for all \( \phi_1, \phi_2 \in \Phi' \) and for all types \( T \in \text{dom}(P \parallel Q) \). It means that for every valuation \( \phi' \in \Phi' \), we can pick a bijection \( g_{\phi'} : \mathcal{V} \rightarrow \mathcal{V} \) such that \( g_{\phi_1} \circ \phi_1(T) = g_{\phi_2} \circ \phi_2(T) \) for all \( \phi_1, \phi_2 \in \Phi' \) and for all types \( T \in \text{dom}(P \parallel Q) \). Since we assumed the valuations in \( \Phi \) to be non-isomorphic, a set \( \Phi'' := \{ g_{\phi'} \circ \phi' \mid \phi' \in \Phi' \} \) must be infinite. However, this is impossible, since each valuation \( \phi'' \in \Phi'' \) covers the same finite set of variables and the values of the variables are always picked from the same finite set \( \text{Im}(\phi'') \). Therefore, we conclude that \( \Phi \) must be finite.

Lemma 31: To prove that \( \Phi \) is finite, assume that it is not. Since all the valuations in \( \Phi \) share the same finite domain and \( |\phi(T)| \leq k \) for all \( \phi \in \Phi \) and for all types \( T \in \text{dom}(P \parallel Q) \), there is an infinite subset \( \Phi' \) of \( \Phi \) such that all valuations in \( \Phi' \) map each type in the domain to a set of the same size, i.e., \( |\phi_1(T)| = |\phi_2(T)| \) for all \( \phi_1, \phi_2 \in \Phi' \) and for all types \( T \in \text{dom}(P \parallel Q) \). It means that for every valuation \( \phi' \in \Phi' \), we can pick a bijection \( g_{\phi'} : \mathcal{V} \rightarrow \mathcal{V} \) such that \( g_{\phi_1} \circ \phi_1(T) = g_{\phi_2} \circ \phi_2(T) \) for all \( \phi_1, \phi_2 \in \Phi' \) and for all types \( T \in \text{dom}(P \parallel Q) \). Since we assumed the valuations in the \( \Phi \) to be non-isomorphic, a set \( \Phi'' := \{ g_{\phi'} \circ \phi' \mid \phi' \in \Phi' \} \) must be infinite. However, this is impossible, since each valuation \( \phi'' \in \Phi'' \) covers the same finite set of variables and the values of the variables are always picked from the same finite set \( \text{Im}(\phi'') \). Therefore, we conclude that \( \Phi \) must be finite.

Proposition 32: By definition, there is a bijection \( g : \mathcal{V} \rightarrow \mathcal{V} \) such that \( \psi = g \circ \phi \). Necessarily, \( g \) preserves the constants in \( I_T \) for every type \( T \). Now, if \( [P]_{\phi} \preceq_{\text{tr}} [Q]_{\phi} \), then by Item 10 of Proposition 23, \( g([P]_{\phi}) \preceq_{\text{tr}} g([Q]_{\phi}) \), too. By Lemma 28, it means that \( [P]_{g \circ \phi} \preceq_{\text{tr}} [Q]_{g \circ \phi} \) or, in other words, \( [P]_{\psi} \preceq_{\text{tr}} [Q]_{\psi} \).

Corollary 21: By Lemma 31, the finiteness of \( \Phi \) is clear. Moreover, if \( P \preceq_{\text{tr}} Q \), then by definition, \( [P]_{\phi} \preceq_{\text{tr}} [Q]_{\phi} \) for all \( \phi \in \Phi \), too. To prove the other direction, let \( \Psi \) be the set of all valuations with the domain \( \text{dom}(P \parallel Q) \), \( \Psi' \) the set of all valuations \( \psi' \in \Psi \) such that \( |\psi'(U)| \leq \text{cd}_U(P \parallel Q) \) for every process type \( U \in \text{dom}(\psi) \) and \( \Psi'' \) the set of all valuations \( \psi'' \in \Psi' \) such that \( |\psi''(T)| \leq \text{cd}_T(P \parallel Q) \) for every data type \( T \in \text{dom}(\psi') \). Now, if \( [P]_{\phi} \preceq_{\text{tr}} [Q]_{\phi} \) for all \( \phi \in \Phi \), then by Proposition 32, \( [P]_{\psi'} \preceq_{\text{tr}} [Q]_{\psi''} \) for all \( \psi'' \in \Psi'' \). By Theorem 20, it implies that then \( [P]_{\psi'} \preceq_{\text{tr}} [Q]_{\psi'} \) for all \( \psi' \in \Psi' \), and by Theorem 16, we see that \( [P]_{\psi} \preceq_{\text{tr}} [Q]_{\psi} \) for all \( \psi \in \Psi \), too. Finally, by Proposition 24, it means that then \( [P]_{\phi} \preceq_{\text{tr}} [Q]_{\phi} \) for all compatible valuations \( \phi \), in other words, \( P \preceq_{\text{tr}} Q \).

Theorem 22: Similar to the proof of Corollary 21 but simpler.
C. Figures

![Diagram](image_url)

Figure 3. SPP Host($x_h$, $y_h$, $T_A$) representing the behaviour of HCP from the viewpoint of two hosts, where $to()$ denotes timeout and $wh(z_h, z_a)$ means that the host $z_a$ wants to know whether someone has the address $z_a$.

D. Verification Models

1) HCP:

```plaintext
type H

type A

var h : H
var h2: H
var a : A
var a2: A

chan timeout
chan whohas : H,A
chan ihave : H,A

plts Host =

lts

I =  timeout() -> I
[] []a:whohas(h2,a) -> I
[] []a:ihave(h2,a) -> I
[] []a:whohas(h,a) -> W(a)

W(a) =  timeout() -> S(a)
[] timeout() -> I
[] ihave(h2,a) -> I
[] whohas(h2,a) -> I
[] []a2:[!a2=a] ihave(h2,a2) -> W(a)
[] []a2:[!a2=a] whohas(h2,a2) -> W(a)

S(a) =  timeout() -> S(a)
[] ihave(h,a) -> S(a)
[] whohas(h2,a) -> R(a)
[] []a2:[!a2=a] whohas(h2,a2) -> S(a)
[] []a2:ihave(h2,a2) -> S(a)

R(a) =  ihave(h,a) -> S(a)
from I
```
plts DifAdr =
  lts
  I = 
      [a2:ihave(h2,a2) -> I
      [] [a: ihave(h,a) -> S1(a)
      S1(a) = 
          [a2:[!a2=a] ihave(h2,a2) -> S1(a)
          [] ihave(h,a) -> S1(a)
  from I

pset WTEv = (_),h,a:{timeout(), whohas(h,a)}

plts Sys = ||h,h2:[!h=h2] Host

plts Spec = ||h,h2:[!h=h2] DifAdr

verify Sys \ WTEv against Spec wrt traces when true

2) SRS:

type U

type R

type D

avar k : U
avar k1 : U
avar k2 : U
avar r : R
avar d : D

chan rdlock : U, R
chan wrlock : U, R
chan unlock : U, R
chan rdbeg : U, R
chan rdend : U, R, D
chan wrbeg : U, R, D
chan wrend : U, R

ltsc User =
  lts
  I = 
      rdlock(k,r) -> R
      [] wrlock(k,r) -> W
      [] tau() -> I
  R = 
      rdbeg(k,r) -> R2
      [] wrlock(k,r) -> W
      [] unlock(k,r) -> I
  R2 = 
      [] d : rdend(k,r,d) -> R
  W = 
      rdbeg(k,r) -> W2
      [] [] d : wrbeg(k,r,d) -> W3
      [] unlock(k,r) -> I
  W2 = 
      [] d : rdend(k,r,d) -> W
  W3 = 
      wrend(k,r) -> W
  from I

ltsc Lock =
  lts
NO = rdlock(k1,r) -> R1
[] wrlock(k1,r) -> W1
[] rdlock(k2,r) -> R2
[] wrlock(k2,r) -> W2
R1 = wrlock(k1,r) -> W1
[] unlock(k1,r) -> NO
[] rdlock(k2,r) -> R12
R2 = wrlock(k2,r) -> W2
[] unlock(k2,r) -> NO
[] rdlock(k1,r) -> R12
W1 = unlock(k1,r) -> NO
W2 = unlock(k2,r) -> NO
R12 = unlock(k1,r) -> R2
[] unlock(k2,r) -> R1
from NO

ltsc Mutex1 =
lts
NO = rdbeg(k,r) -> R1
[] [] d : wrbeg(k,r,d) -> N1
N1 = wrend(k,r) -> NO
R1 = [] d : rdend(k,r,d) -> NO
from NO

ltsc Mutex2 =
lts
NO = rdbeg(k1,r) -> R1
[] rdbeg(k2,r) -> R2
[] [] d : wrbeg(k1,r,d) -> N1
[] [] d : wrbeg(k2,r,d) -> N2
N1 = wrend(k1,r) -> NO
N2 = wrend(k2,r) -> NO
R1 = [] d : rdend(k1,r,d) -> NO
[] rdbeg(k2,r) -> RR
R2 = [] d : rdend(k2,r,d) -> NO
[] rdbeg(k1,r) -> RR
RR = [] d : rdend(k1,r,d) -> R2
[] [] d : rdend(k2,r,d) -> R1
from NO

ltsc SRS = (|| k, r : User) || (|| r, k1, k2 : ![k1=k2] Lock)
ltsc Mutex = (|| k1, k2, r : ![k1=k2] Mutex2) || (|| k, r : Mutex1)
ssc LcEv = (_) k, r : {rdlock(k,r), wrlock(k,r), unlock(k,r)}

verify SRS \ LcEv against Mutex wrt traces when true

3) Cache:
type P
type R
type D

var p : P
var p2 : P
var r : R
var r2 : R
var d : D
var d2 : D

chan rdbeg : P,R
chan rdend : P,R,D
chan wrbeg : P,R,D
chan wrend : P,R
chan rdmem : P,R,D
chan flush : R,D

plts CUnit =
  lts
  I =
    rdbeg(p,r) -> IR
    [ ] [ ] d : wrbeg(p,r,d) -> IW(d)
    [ ] rdbeg(p2,r) -> I
    [ ] [ ] d : wrbeg(p2,r,d) -> I
    [ ] [ ] d : flush(r,d) -> I
  IR = [ ] d : rdmem(p,r,d) -> IR2(d)
    [ ] rdbeg(p2,r) -> IR
  IR2(d) = rdend(p,r,d) -> S(d)
    [ ]
  IW(d) = wrend(p,r) -> M(d)
    [ ]
  S(d) = rdbeg(p,r) -> SR(d)
    [ ] rdbeg(p2,r) -> S(d)
    [ ] [ ] d : wrbeg(p,r,d) -> SW(d)
    [ ] flush(r,d) -> I
    [ ] tau() -> I
  SR(d) = rdend(p,r,d) -> S(d)
    [ ]
  SW(d) = wrend(p,r) -> M(d)
    [ ]
  M(d) = rdbeg(p,r) -> MR(d)
    [ ] [ ] d : wrbeg(p,r,d) -> MW(d)
    [ ] flush(r,d) -> S(d)
    [ ] flush(r,d) -> I
  MR(d) = rdend(p,r,d) -> S(d)
    [ ]
  MW(d) = wrend(p,r) -> M(d)
    [ ]
  from I

plts Cache = || p,p2,r : ![p=p2] CUnit

plts MUnit =
  lts
    I =
      tau() -> M(d)
    M(d) =
      rdmem(p,r,d) -> M(d)
      [ ] [ ] d2 : flush(r,d2) -> M(d2)
    from I

plts Mem = || p,r : MUnit
plts CacheMem = Cache || Mem

plts Mut2 =
lt

I = rdbeg(p,r) \rightarrow R1
    [ ] rdbeg(p2,r) \rightarrow R2
    [ ] [ ] d : wrbeg(p,r,d) \rightarrow W1
    [ ] [ ] d : wrbeg(p2,r,d) \rightarrow W2
R1 = [ ] d : rdend(p,r,d) \rightarrow I
    [ ] rdbeg(p2,r) \rightarrow R12
R2 = [ ] d : rdend(p2,r,d) \rightarrow I
    [ ] rdbeg(p,r) \rightarrow R12
R12 = [ ] d : rdend(p,r,d) \rightarrow R2
    [ ] [ ] d : rdend(p2,r,d) \rightarrow R1
W1 = wrend(p,r) \rightarrow I
W2 = wrend(p2,r) \rightarrow I
from I


pset MemAcc = (_) p,r,d : {rdmem(p,r,d), flush(r,d)}

verify CacheMem \ MemAcc against Mutex wrt traces when true